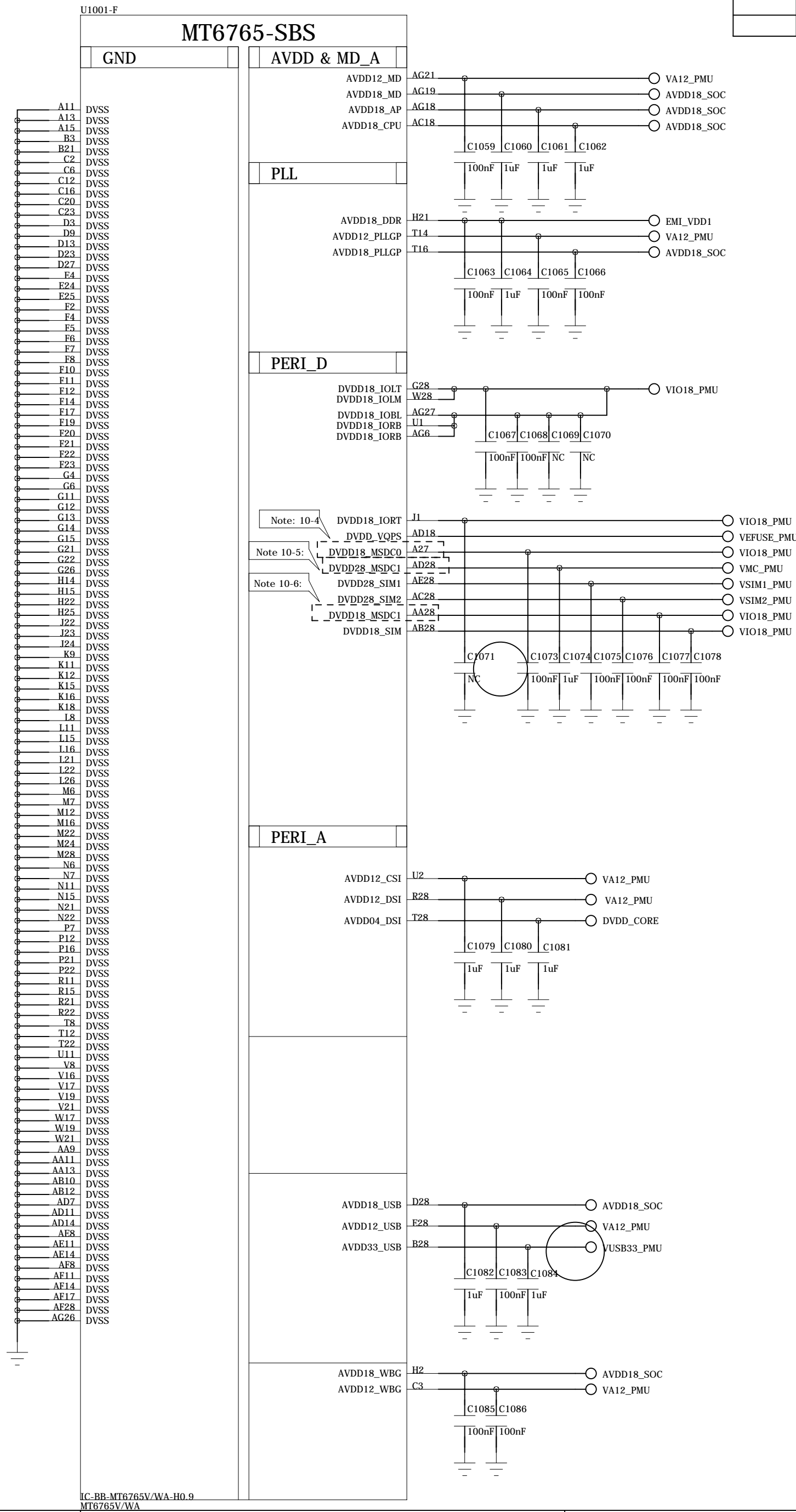


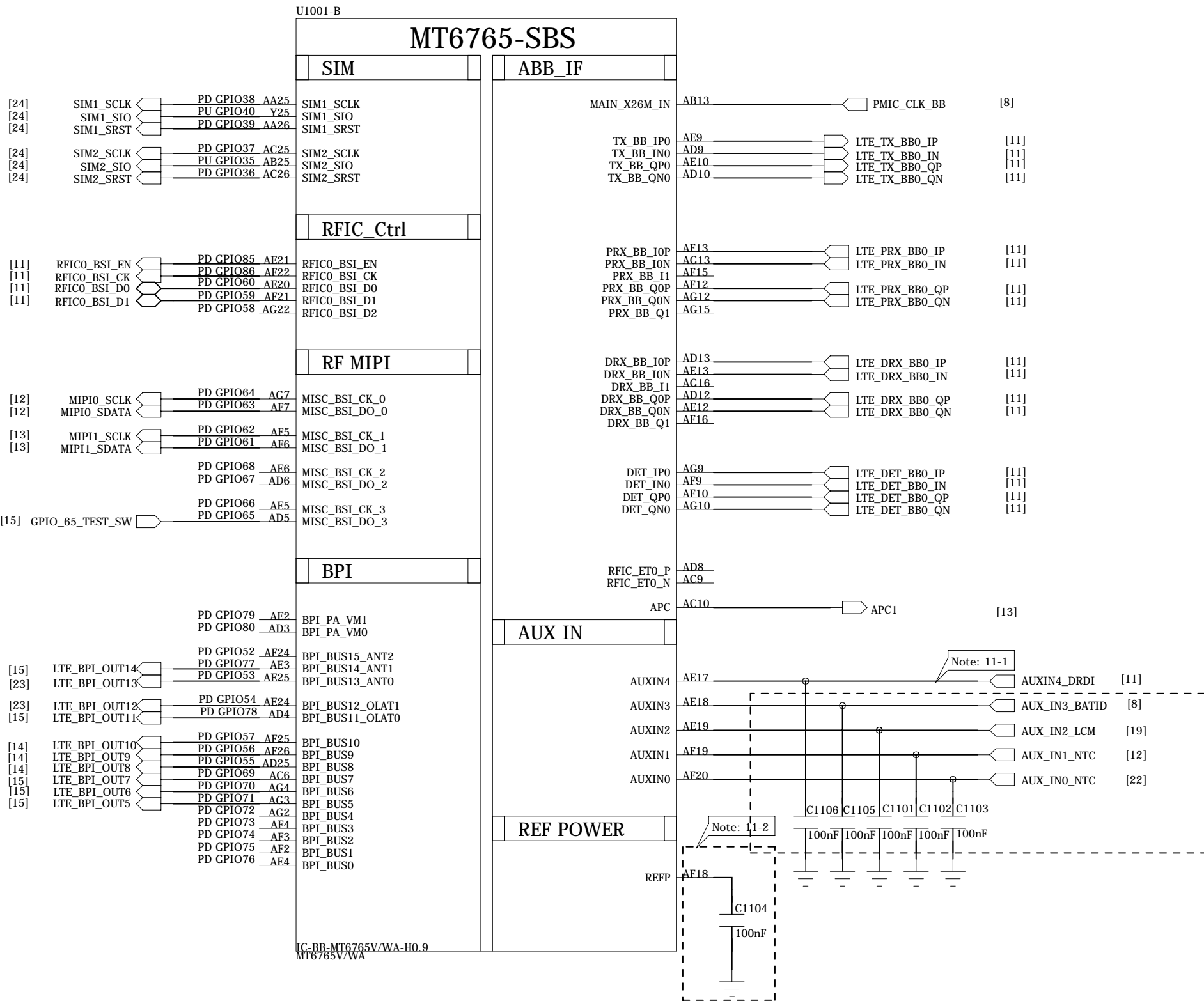
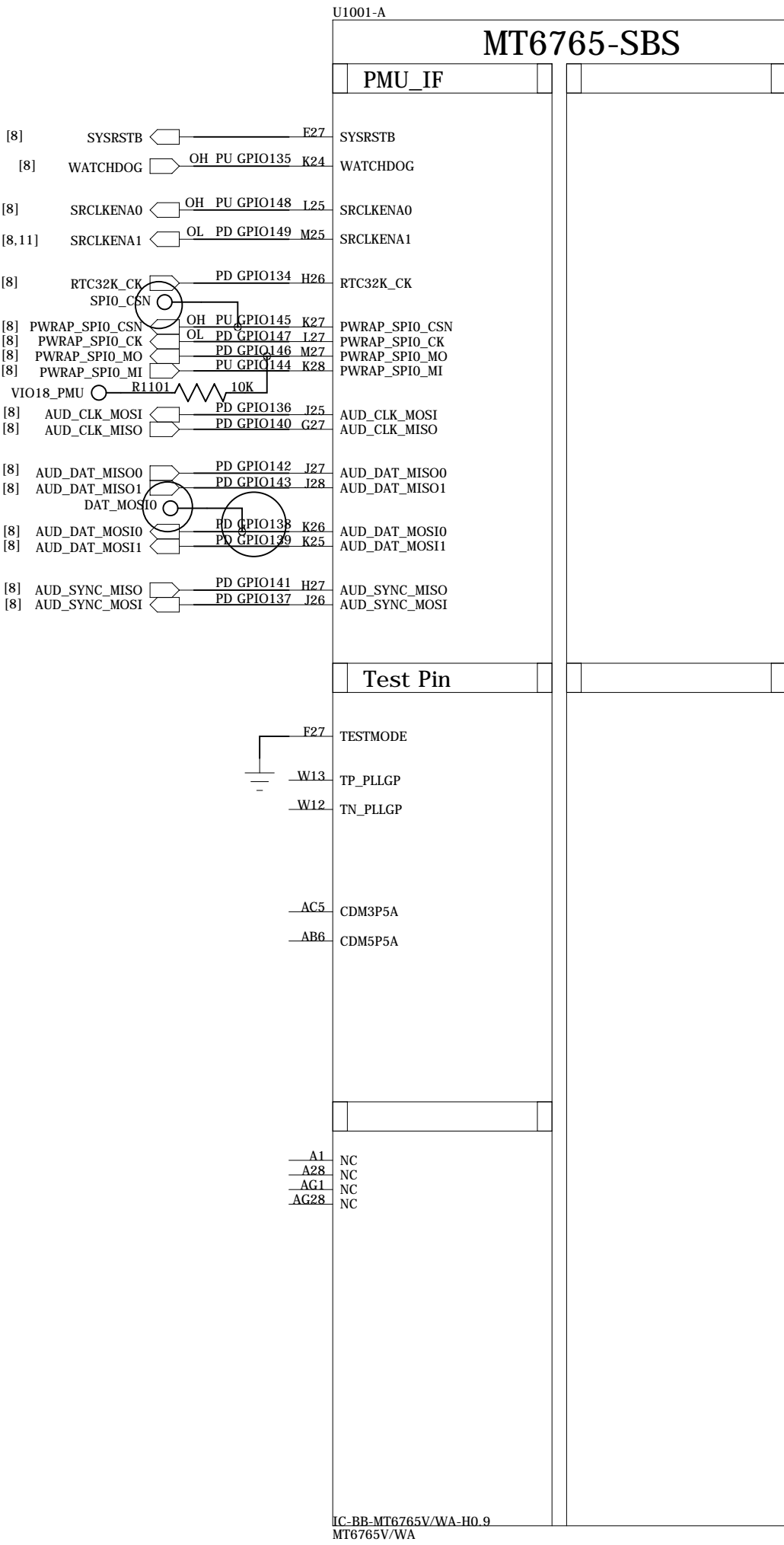
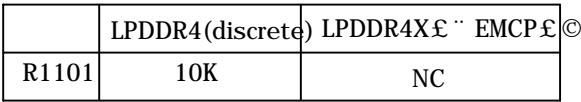
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 10_BB_POWER		VERSION: V1.0	SHEET: 3 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

BB_I

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



PWRRP_SPI0_CSN and *AUD_DAT_MOSIO* are bootstrap pin to select which interface will be the JTAG pin out.						
Default	PWRRP_SPI0_CSN		AUD_DAT_MOSIO		AP_JTAG	IO_JTAG
	(PU)	HI	(PD)	LO	N/A	N/A
		HI		HI	SPI_CSB/SPI_CLK/SPI_M0/SPI_M1/EINT8	N/A
		LO		LO	SPI_CSB/SPI_CLK/SPI_M0/SPI_M1/EINT8	SP1 + SP3
		LO		LO	MSDC1_CLK/CMD/DAT0/DAT1/DAT2	N/A

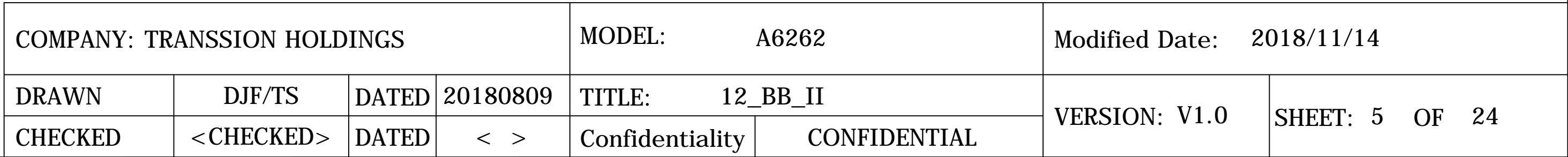
Note 22-4: PWRAP_SPI0_MI are DDR type feature in bootstrap				
	PWRAP_SPI0_MI	Bootling interface		
	Default=PU	DDR	MSDC0 pin mux	
Default	LO <small>(do not, PE)</small>	LPDDR3	follow LP3	Ref SCH.
	HI	LPDDR4X	follow LP4X	Ref SCH.

COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 11_BB_I		VERSION: V1.0	SHEET: 4 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

Schematic design notice of "11_BB_1" page.

Note 11-1: To shunt a 1uF capacitor in the AUXIN ADC input to prevent noise coupling. It should be placed as close to BB as possible.
Connect the unused AUX ADC input to GND.

Note 11-2: The de-coupling cap. for REFF (AF18 ball) have to be placed as close to BB as possible.



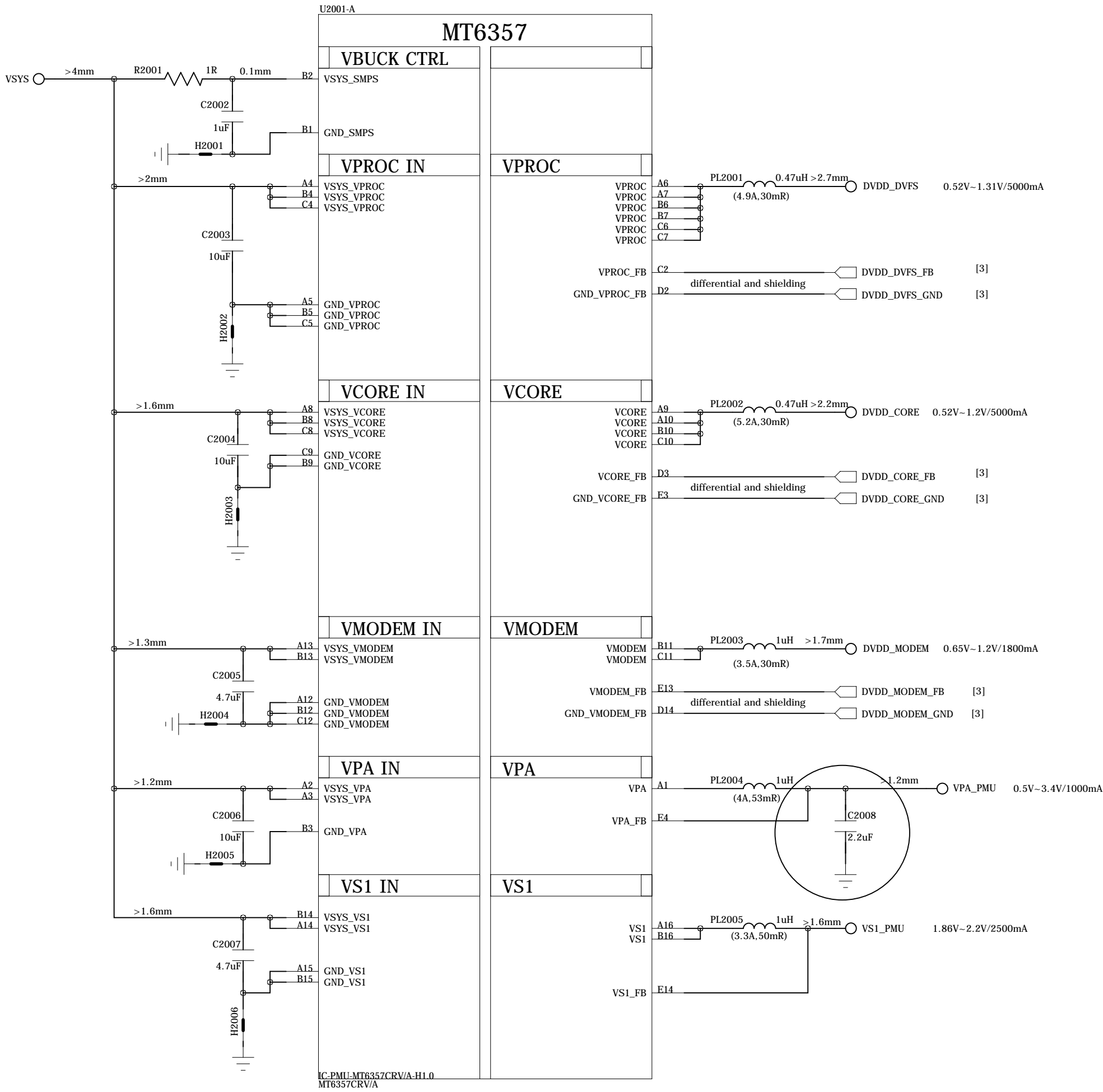
[illegible]

COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 13_BB_III		VERSION: V1.0	SHEET: 6 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

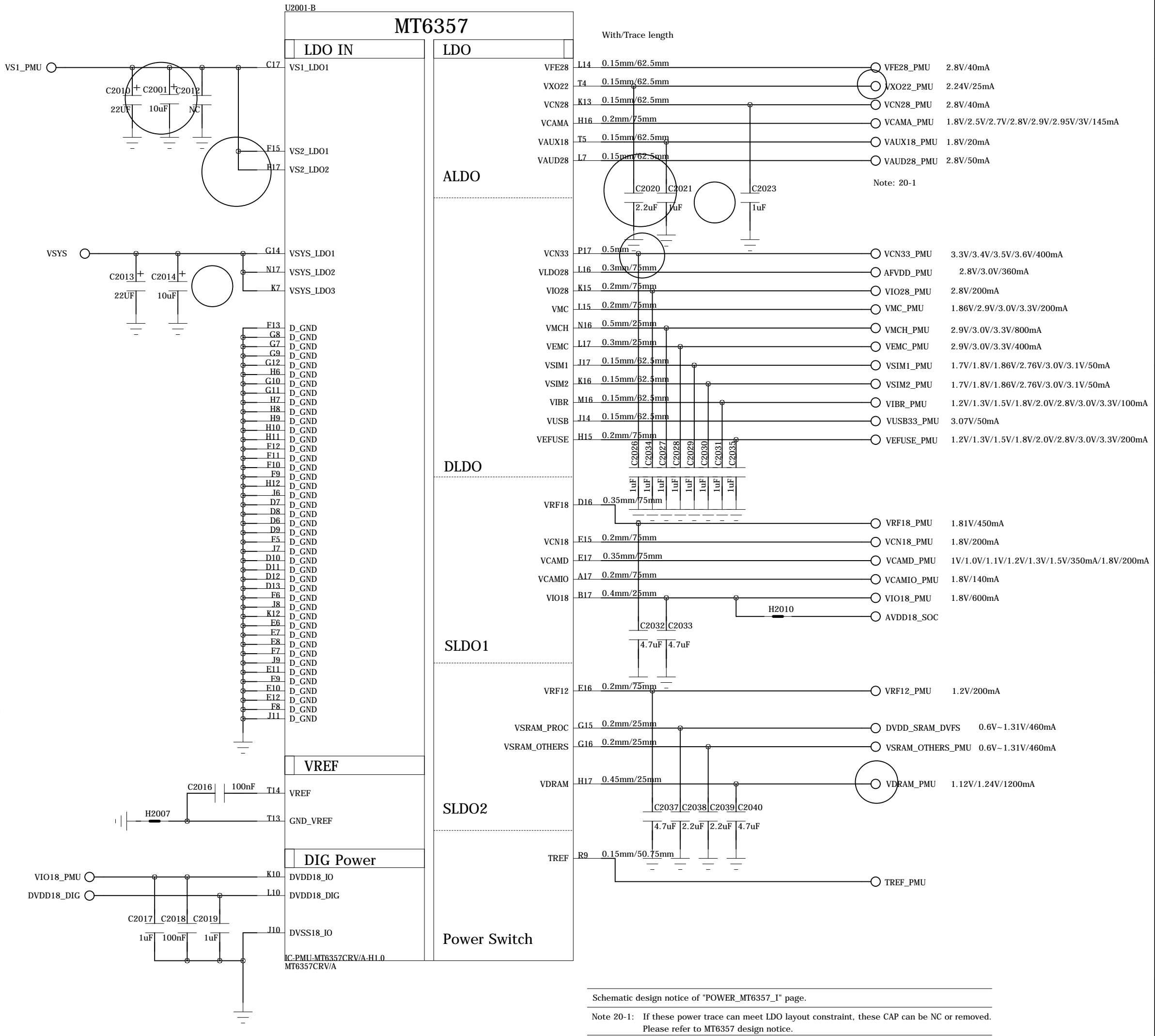
POWER_MT6357_I

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

PMU_BUCK



PMU_LDO



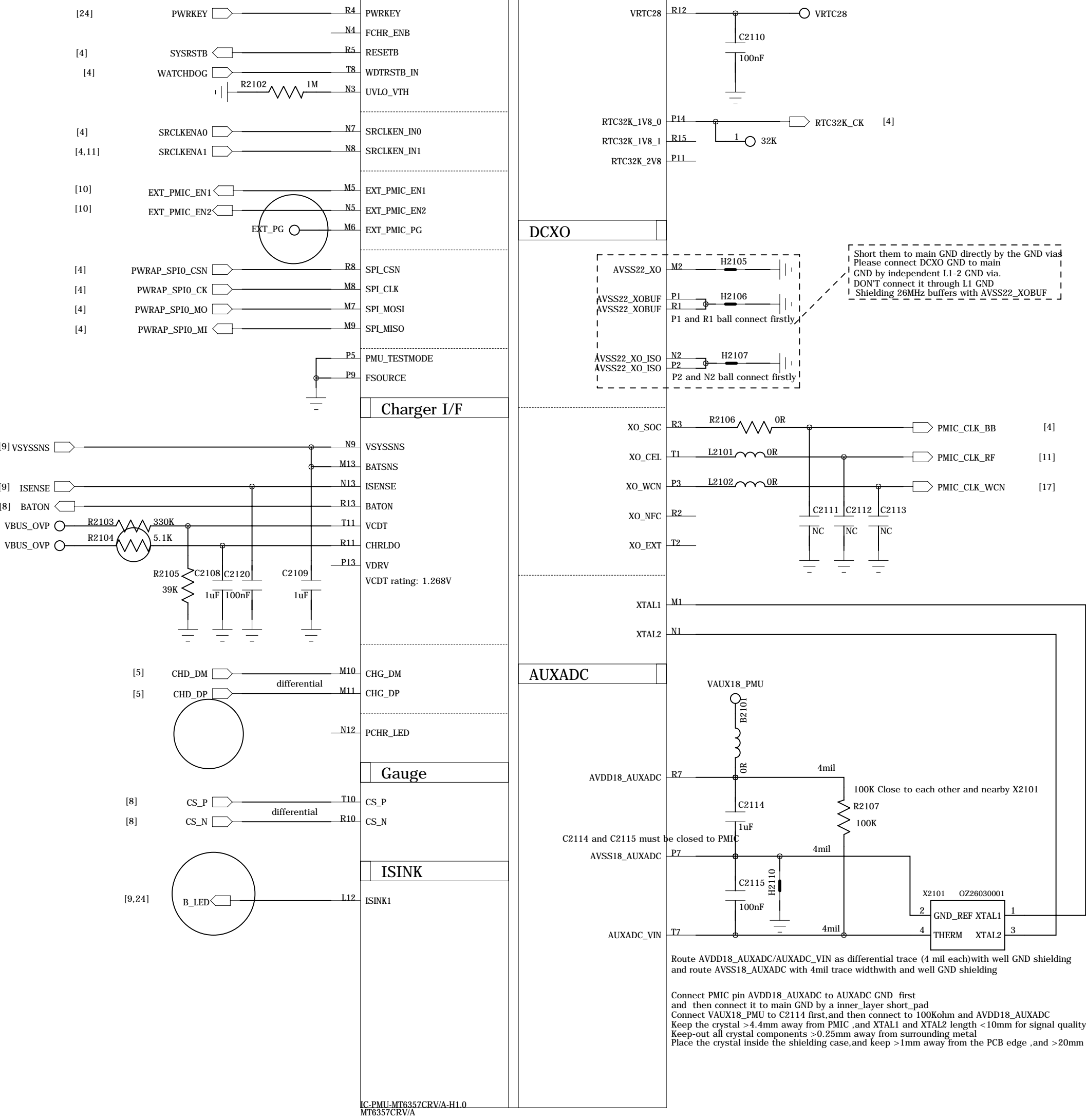
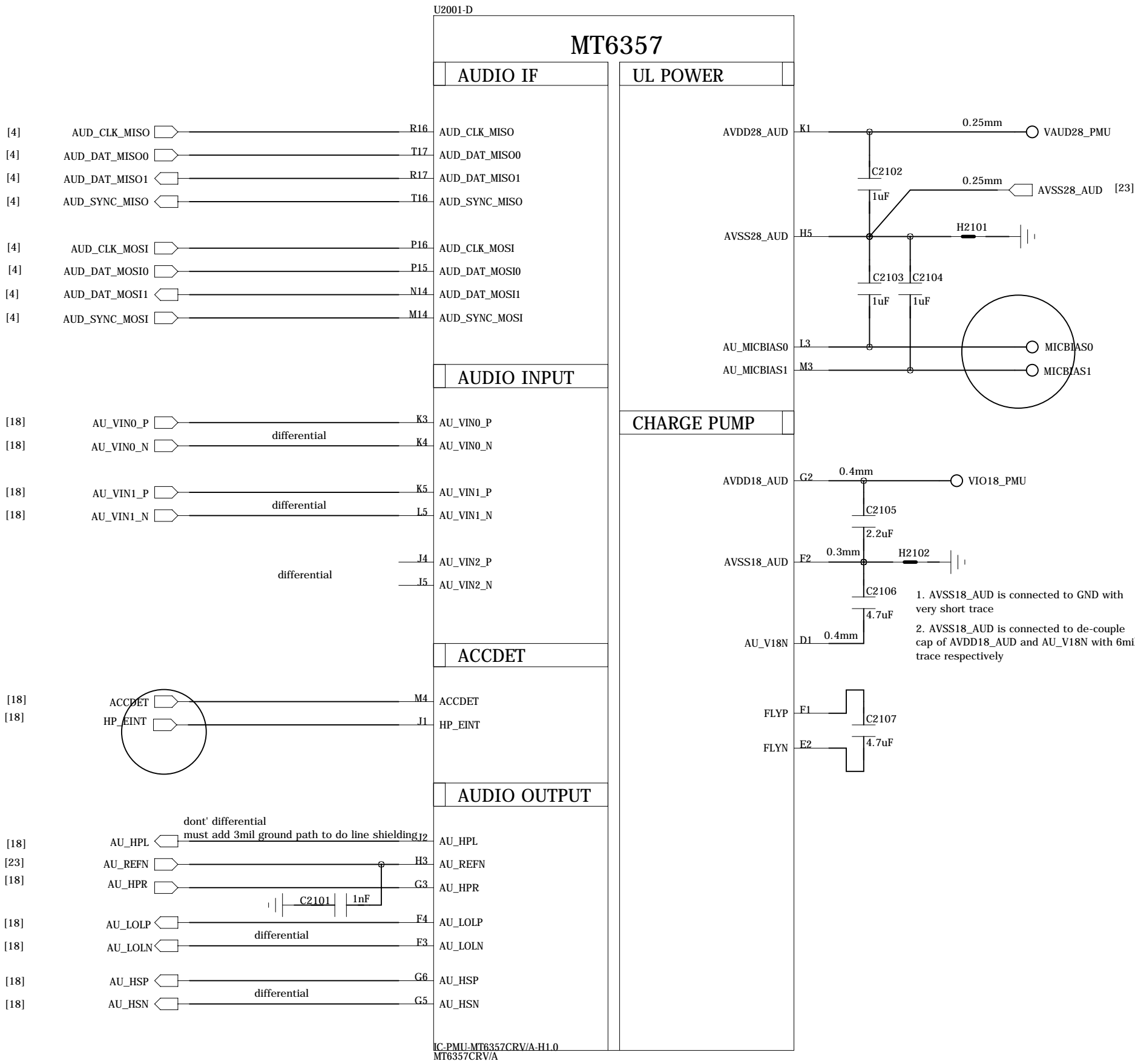
COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 20_POWER_MT6357_I		VERSION: V1.0	SHEET: 7 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

POWER_MT6357_II

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

PMU_CLK

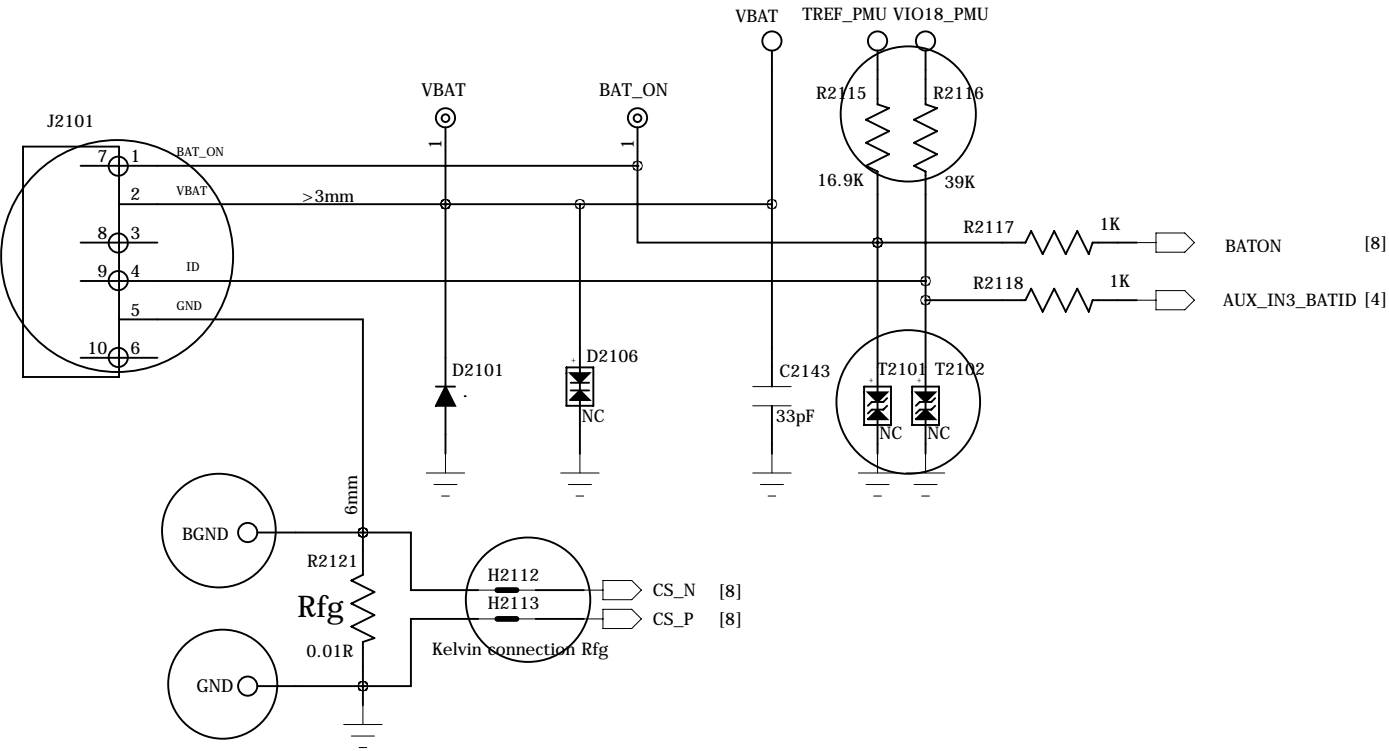
PMU_AUDIO



Route AVDD18_AUXADC/AUXADC_VIN as differential trace (4 mil each)with well GND shielding and route AVSS18_AUXADC with 4mil trace widthwith and well GND shielding

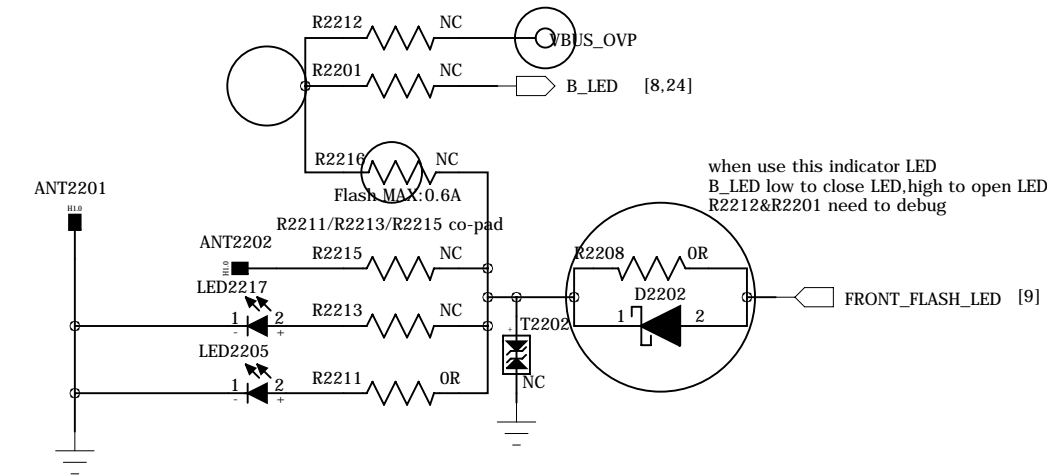
Connect PMIC pin AVDD18_AUXADC to AUXADC GND first and then connect it to main GND by a inner_layer short_pad
Connect VAUX18_PMU to C2114 first, and then connect to 100Kohm and AVDD18_AUXADC
Keep the crystal >4.4mm away from PMIC, and XTAL1 and XTAL2 length <10mm for signal quality consideration
Keep-out all crystal components >0.25mm away from surrounding metal
Place the crystal inside the shielding case, and keep >1mm away from the PCB edge , and >20mm away from USB , headpho

BATTERY CONNECTOR



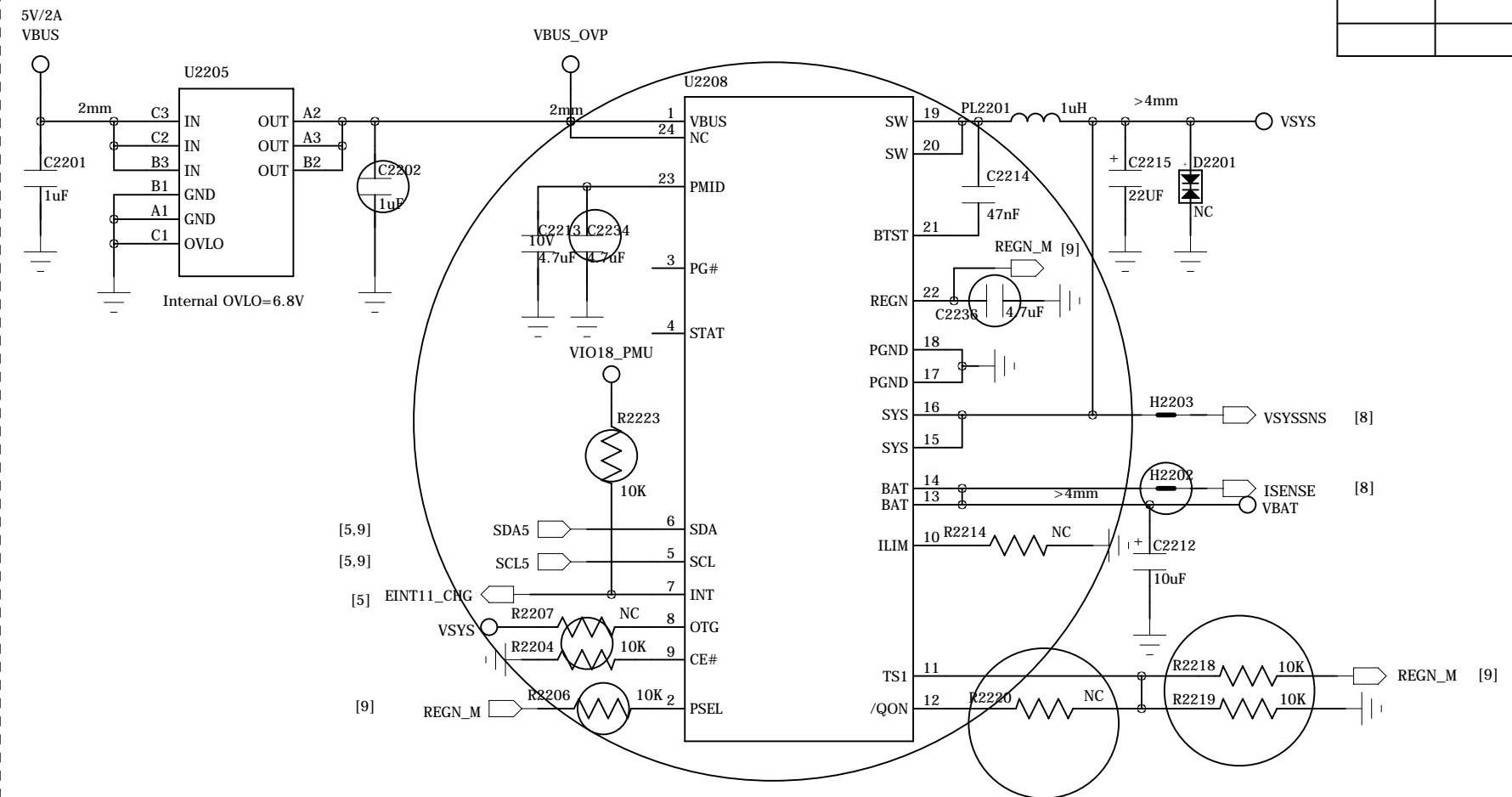
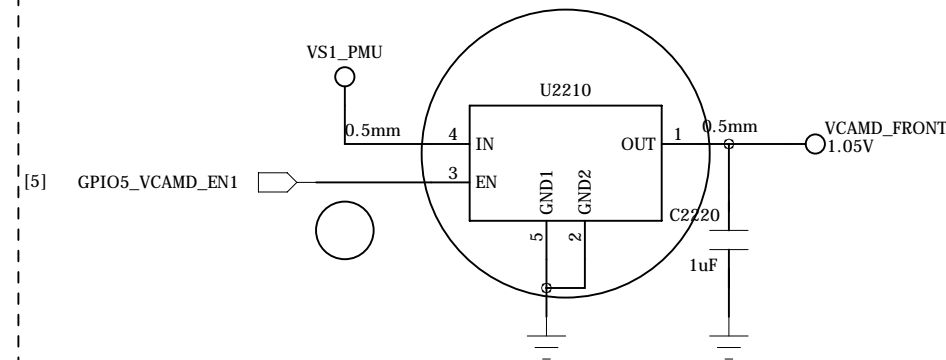
COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 21_POWER_MT6357_II		VERSION: V1.0	SHEET: 8 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

POWER_THIRD-PARTY_I
FRONT CAMERA FLASH LED

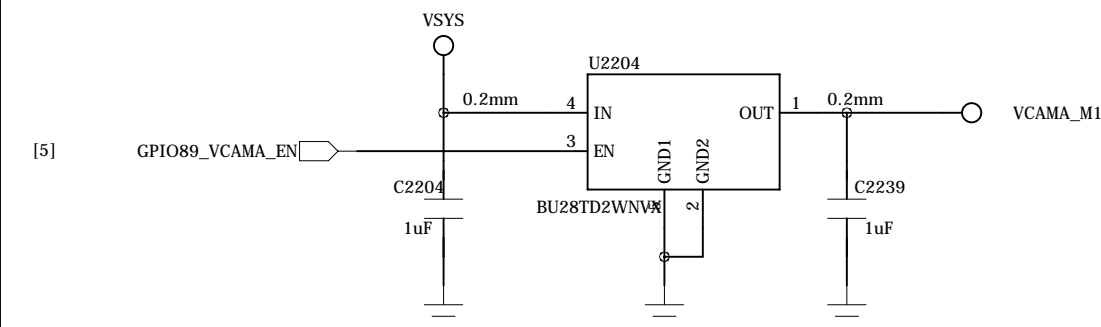


	LED2205 R2211	LED2217 R2213	ANT2201 ANT2202 R2215
Notch w/ 8M front camera	NC	Yes	NC
Notch w/ big than 8M front camera	Yes	NC	NC
Notch w/ two front cameras	NC	NC	Yes

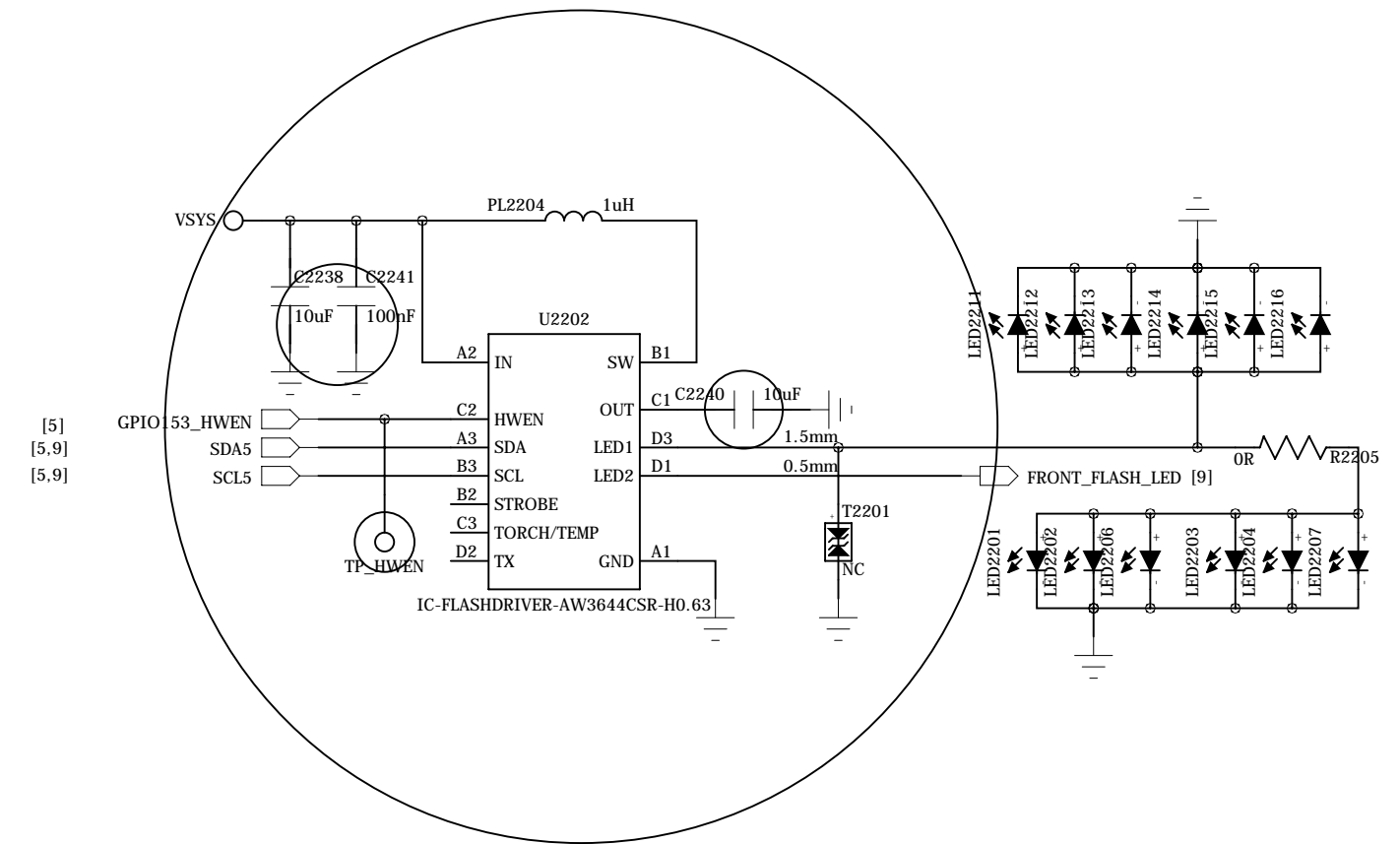
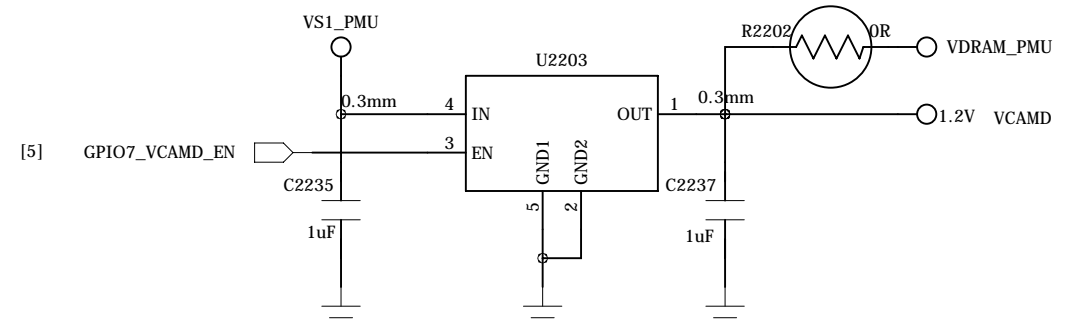
VCAMD_SUB (1.05V)



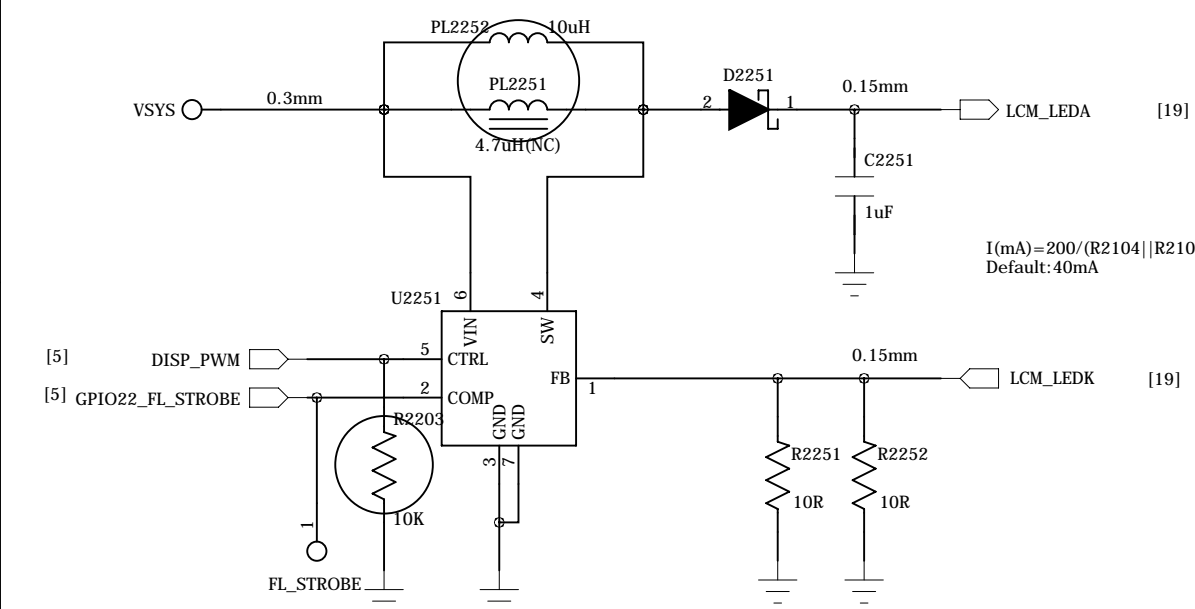
VCAMA_MAIN(2.8V)



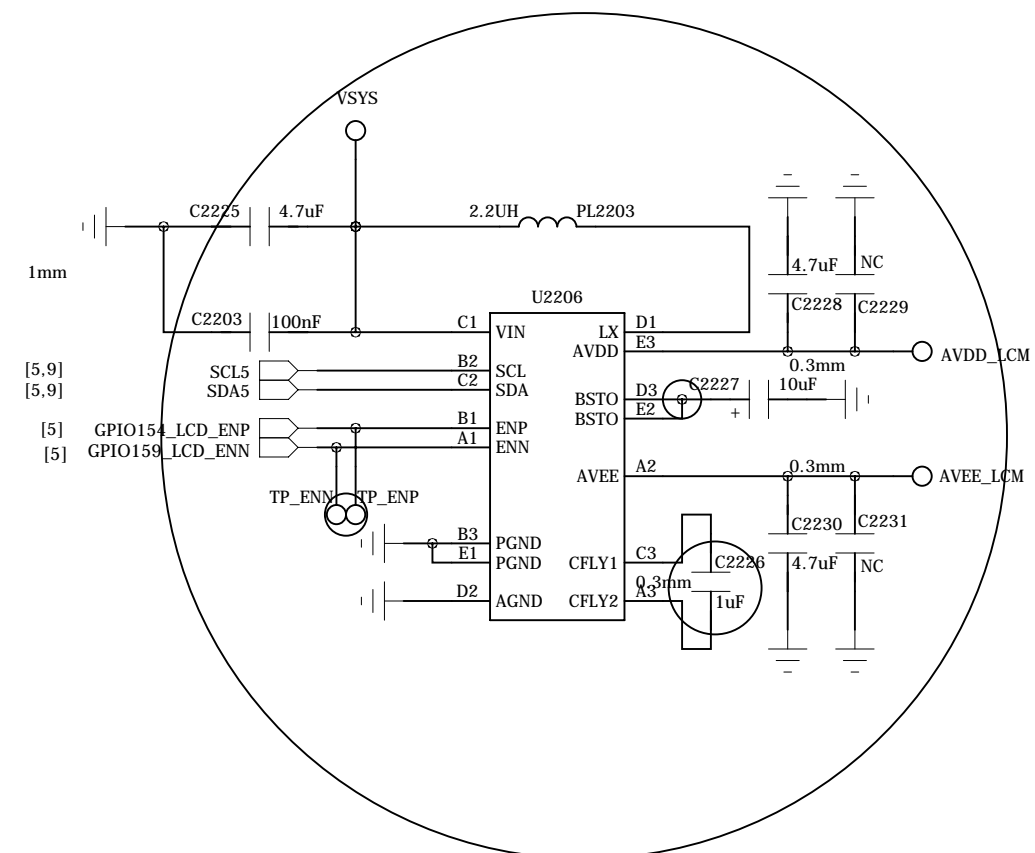
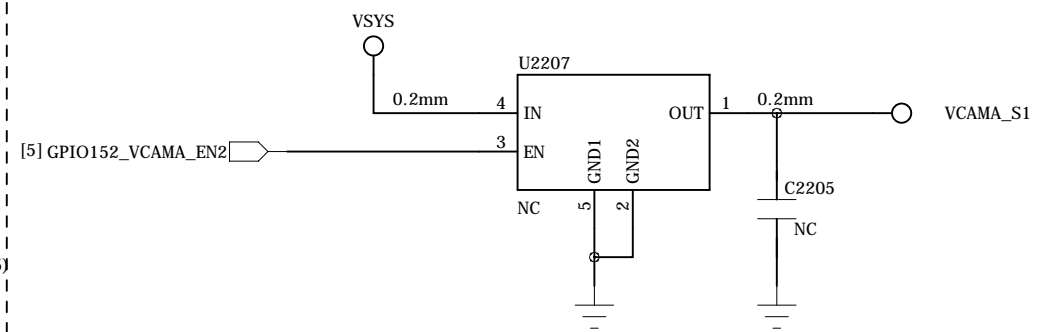
VCAMD_SUB (1.2V)



Backlight and face flash



VCAMA_SUB(2.8V)

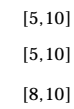


COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 22_POWER_THIRD-PARTY_I		VERSION: V1.0	SHEET: 9 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

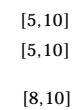
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

I2C address: 0X57 (Write:0xAE, Read:0xAF)



[8]

I2C address: 0x50 (Write:0xA0, Read:0xA1)

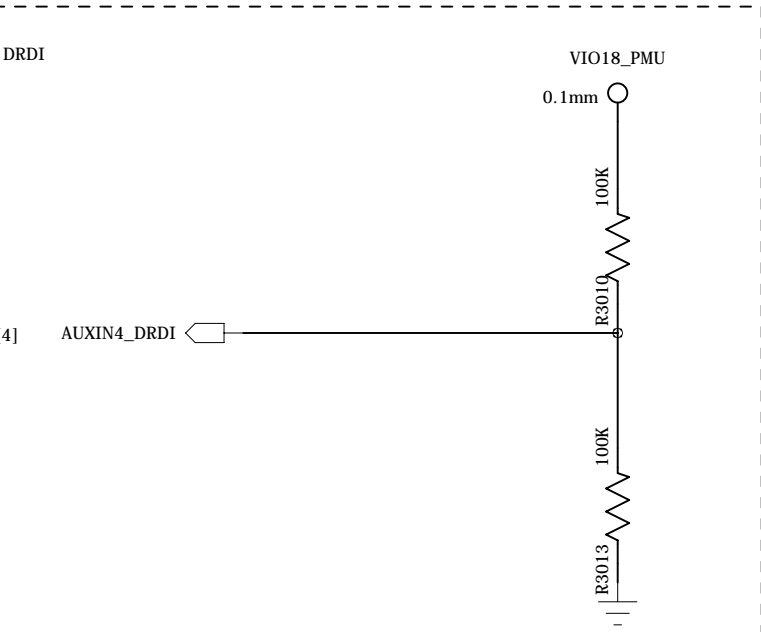
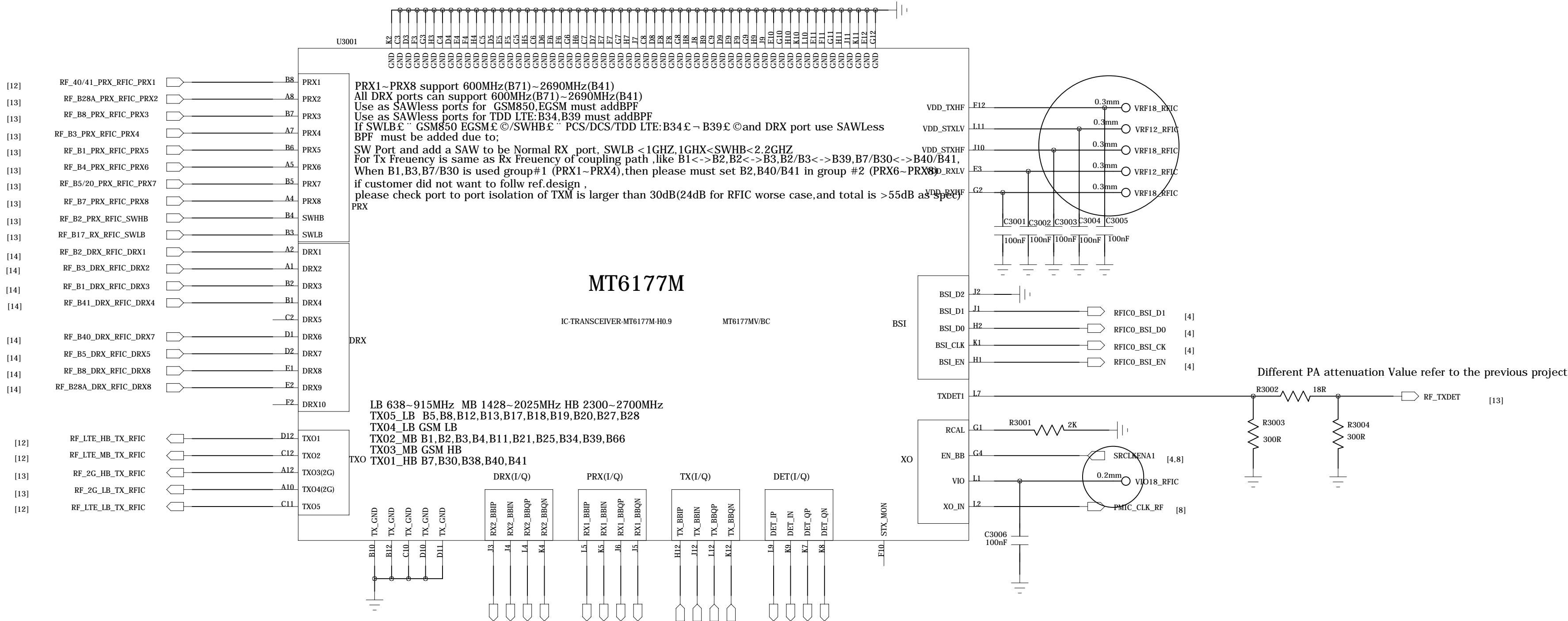


VIO18

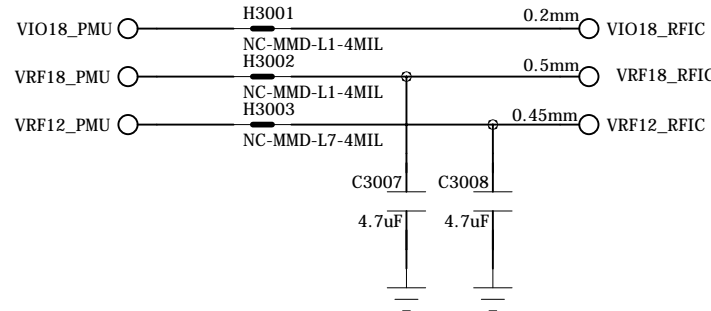
COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 23_POWER_THIRD-PARTY_II		VERSION: V1.0	SHEET: 10 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_PIN_OUT

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



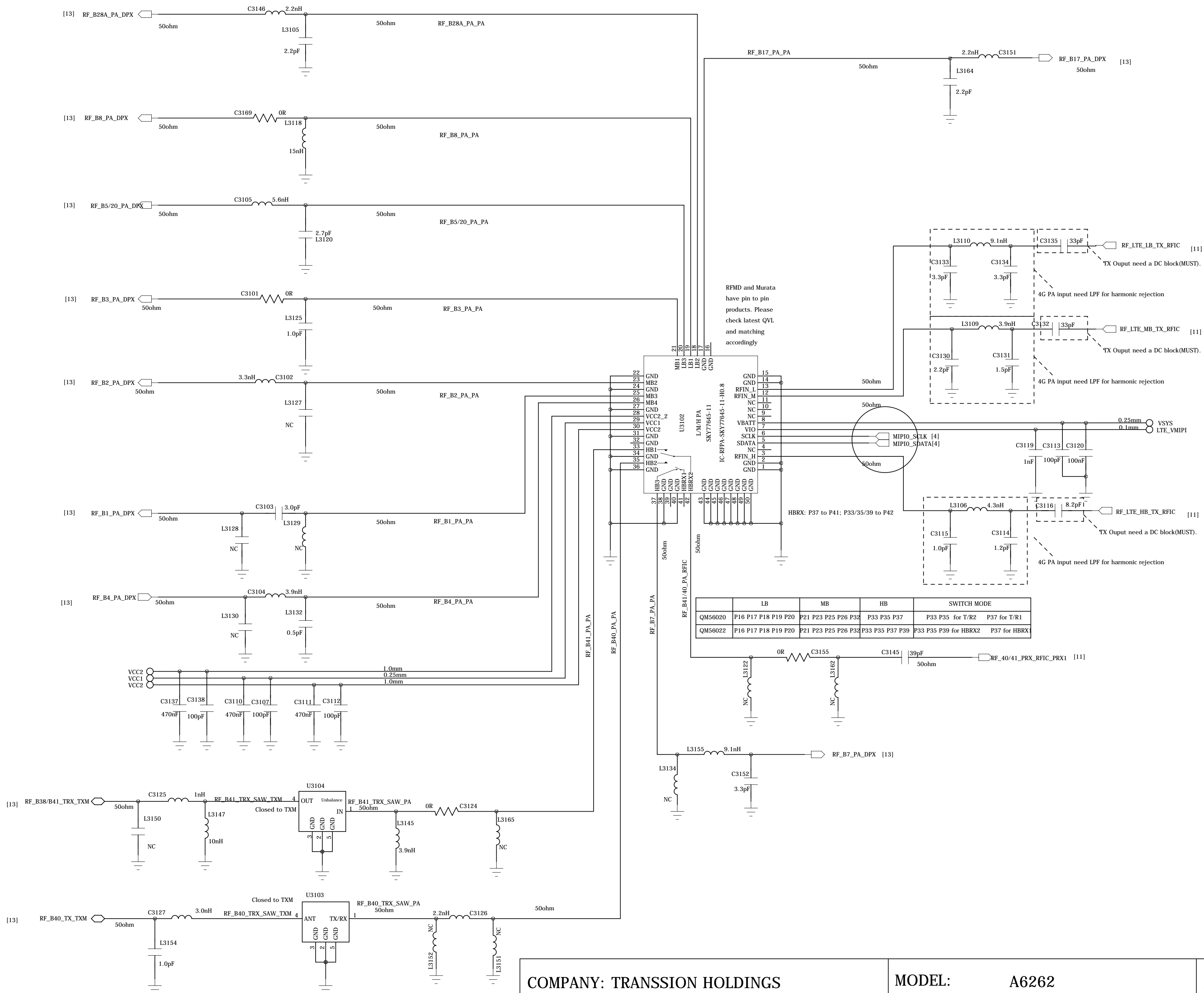
Power domain of MT6177M



COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 30_RF_MT6177M_PIN_OUT		VERSION: V1.0	SHEET: 11 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6176_RF_TX

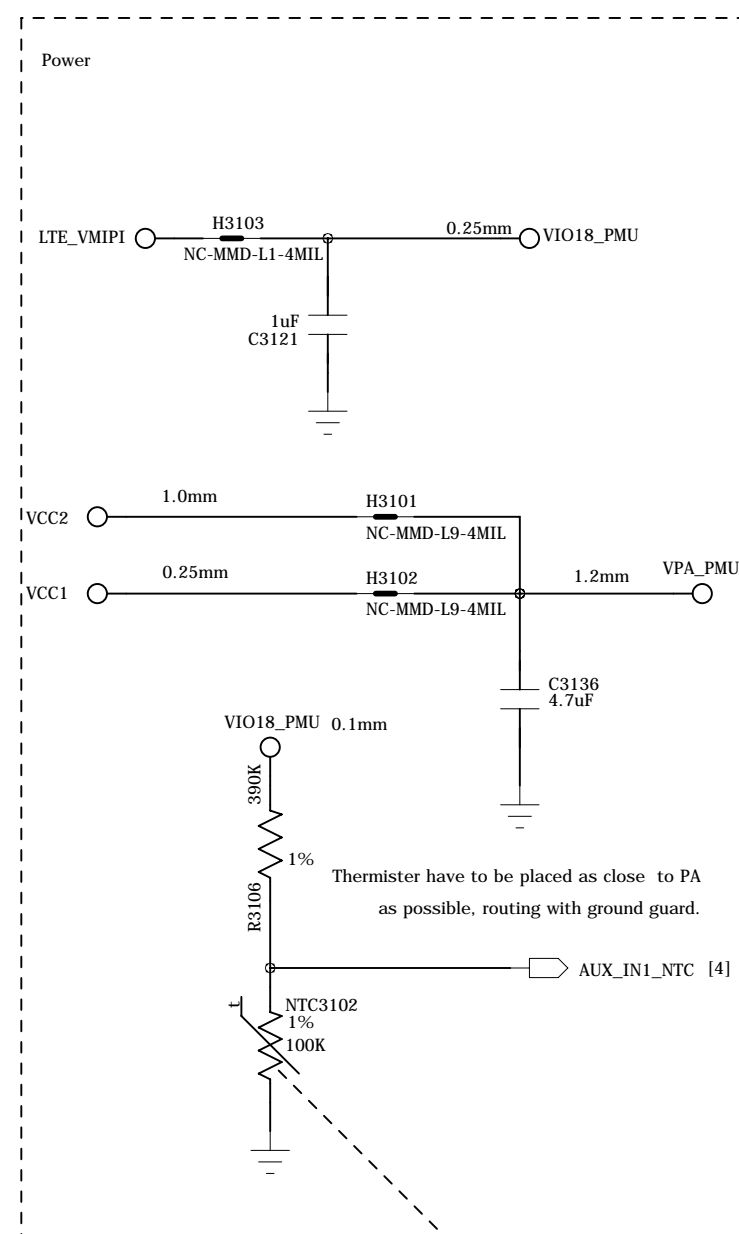
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



3/4G_PAIN_LB

3/4G_PAIN_MB

3/4G_PAIN_HB



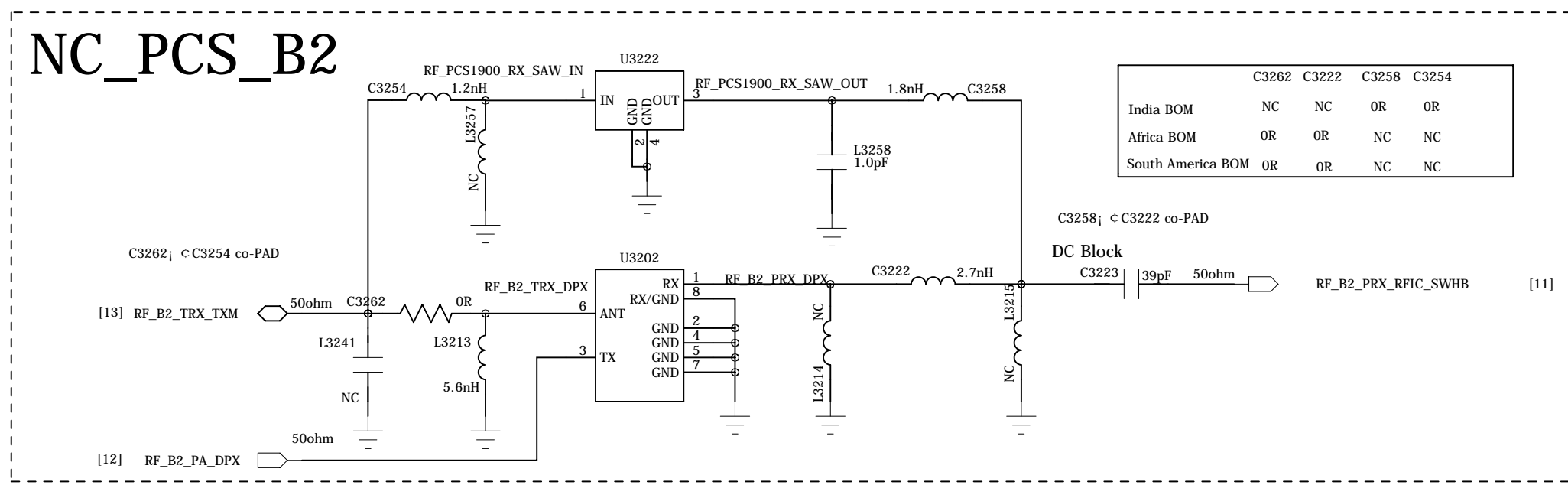
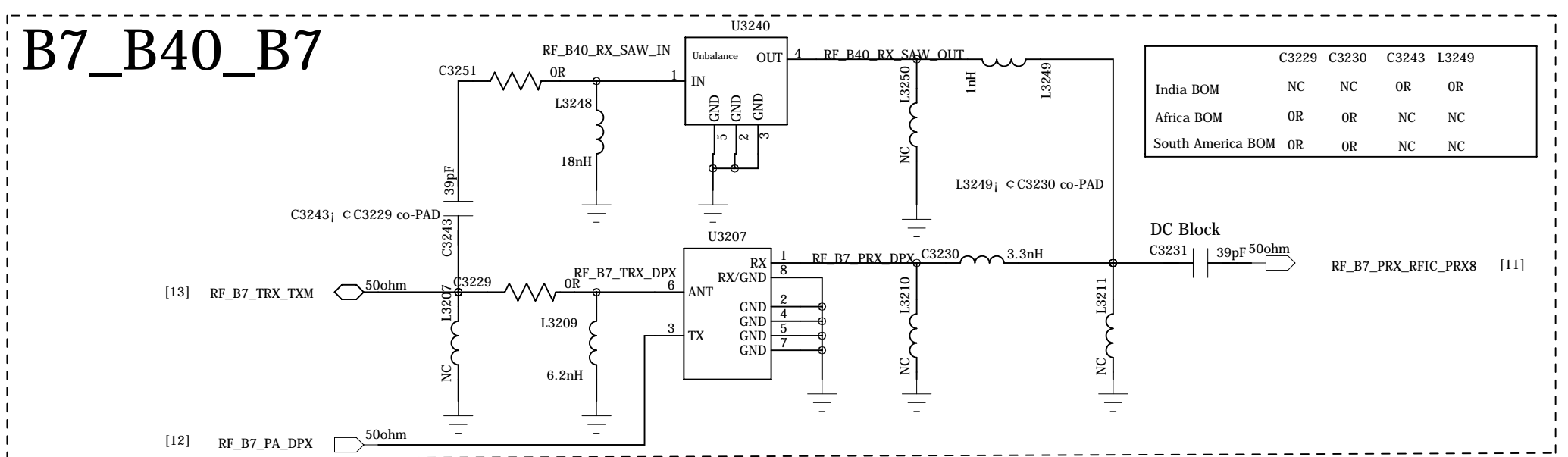
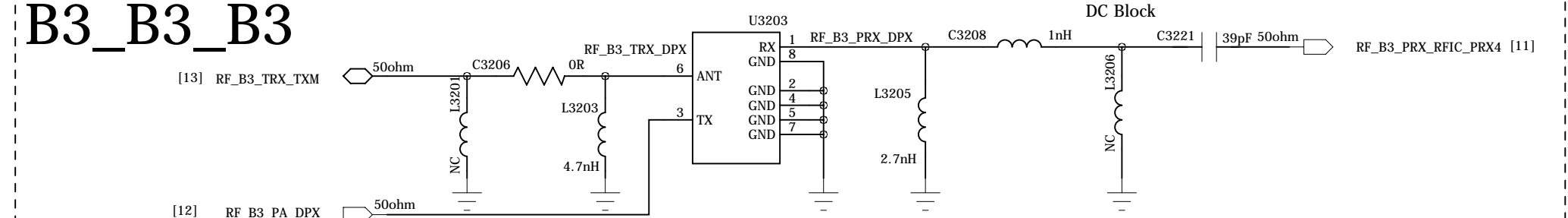
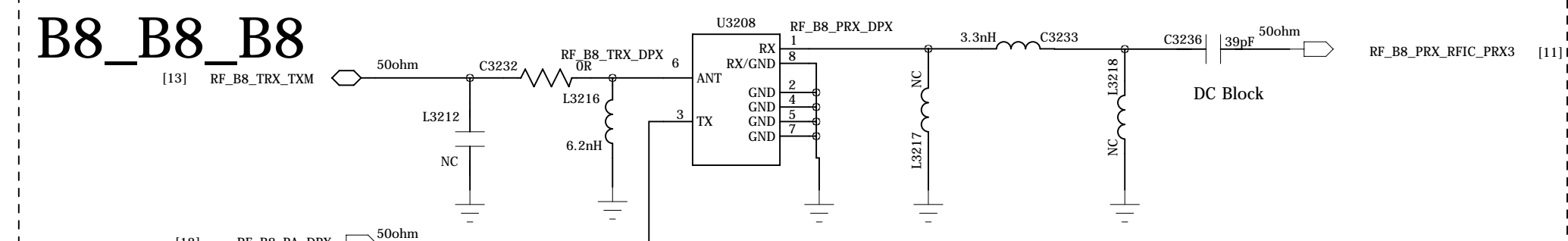
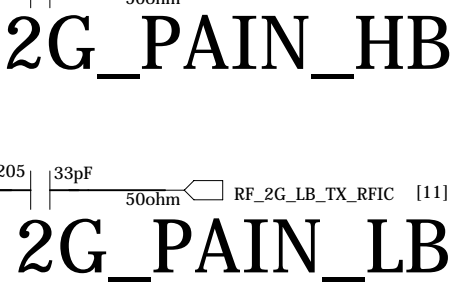
COMPANY: TRANSSION HOLDINGS				MODEL: A6262	
DRAWN	DJF/TS	DATED	20180809	TITLE: 31_RF_MT6177M_RF_TRX	
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL

Modified Date: 2018/11/14

VERSION: V1.0

SHEET: 12 OF 24

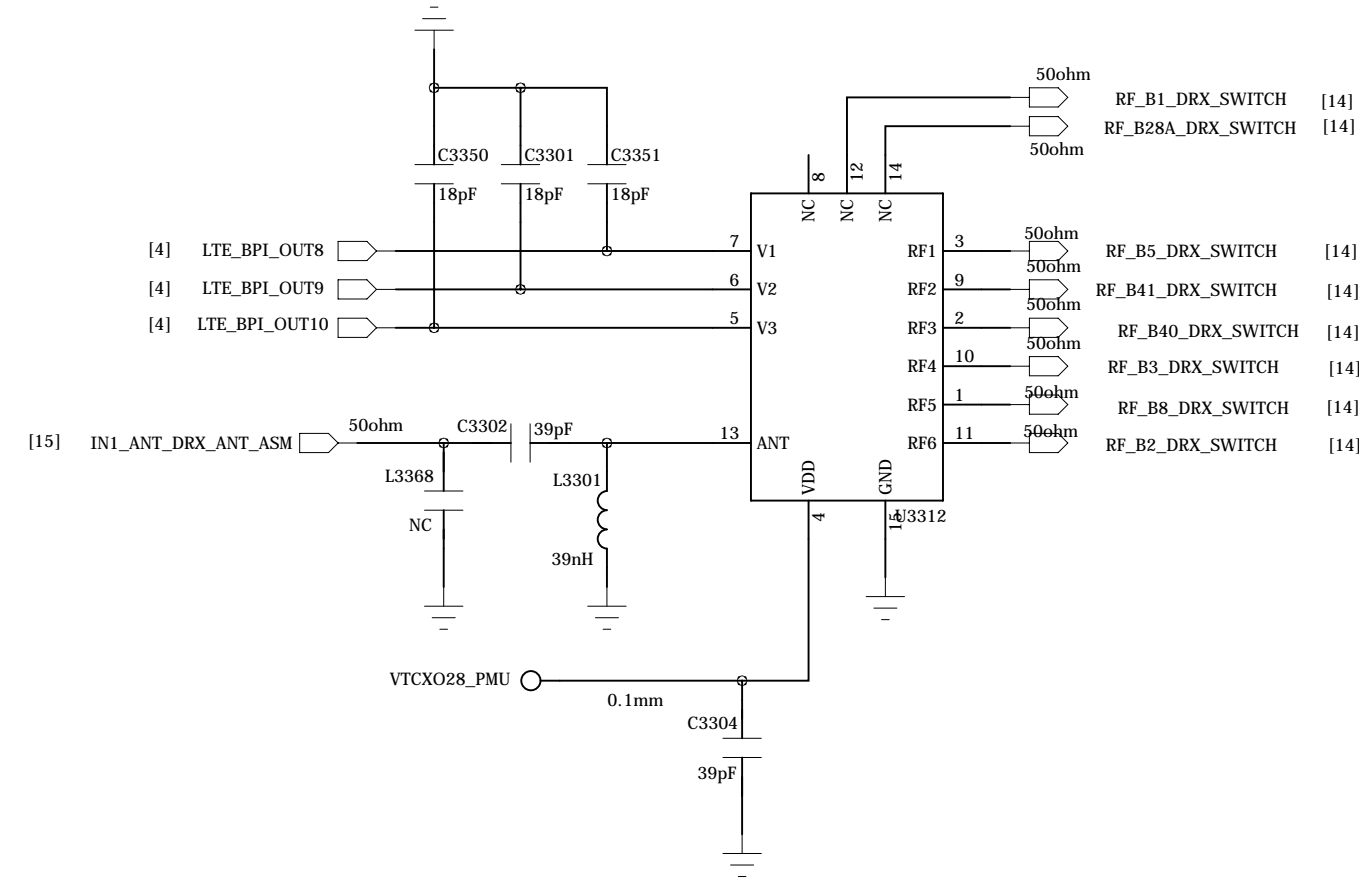
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



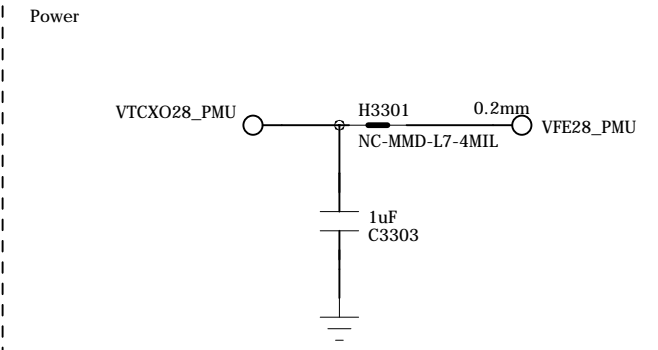
COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 32_RF_MT6177M_RF_PRX		VERSION: V1.0	SHEET: 13 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_RF_DRX

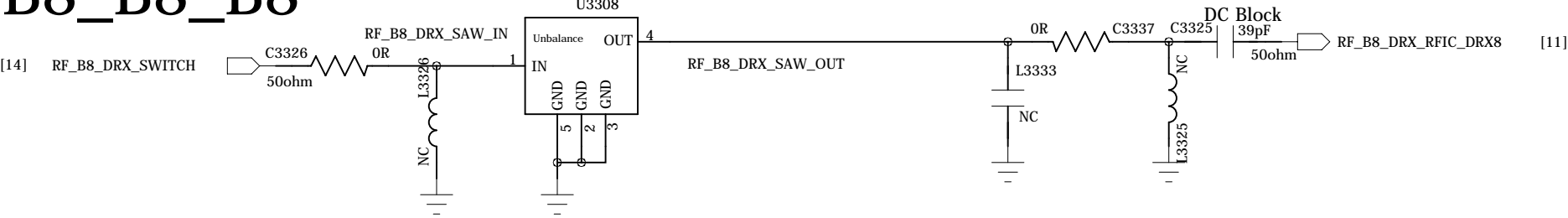
SP8T



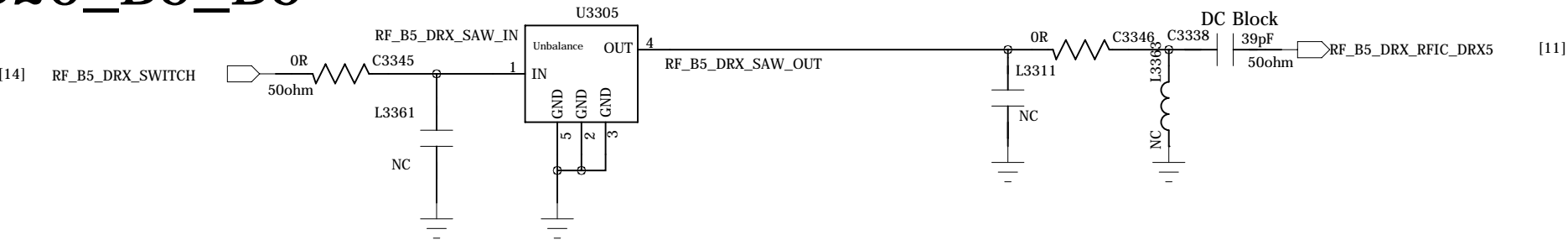
SP8T control logic										VC1613 control logic					
SP6T control logic											VC1	VC2	RF1	RF2	RF3
V1	V2	V3	RF1	RF2	RF3	RF4	RF5	RF6	RF7	RF8	H	L	Y	N	N
L	L	L	Y	N	N	N	N	N	N	N	H	H	N	Y	N
L	L	H	N	Y	N	N	N	N	N	N	N	H	N	N	Y
L	H	L	N	N	Y	N	N	N	N	N	N	H	N	N	Y
L	H	H	N	N	N	Y	N	N	N	N	N	H	N	N	Y
H	L	L	N	N	N	N	Y	N	N	N	N	H	N	N	Y
H	L	H	N	N	N	N	N	Y	N	N	N	H	N	N	Y
H	H	L	N	N	N	N	N	N	Y	N	N	H	N	N	Y
H	H	H	N	N	N	N	N	N	N	Y	N	H	N	N	Y



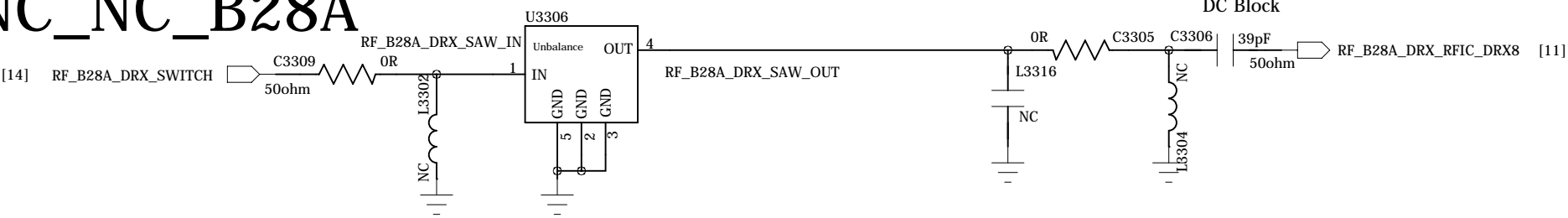
B8_B8_B8



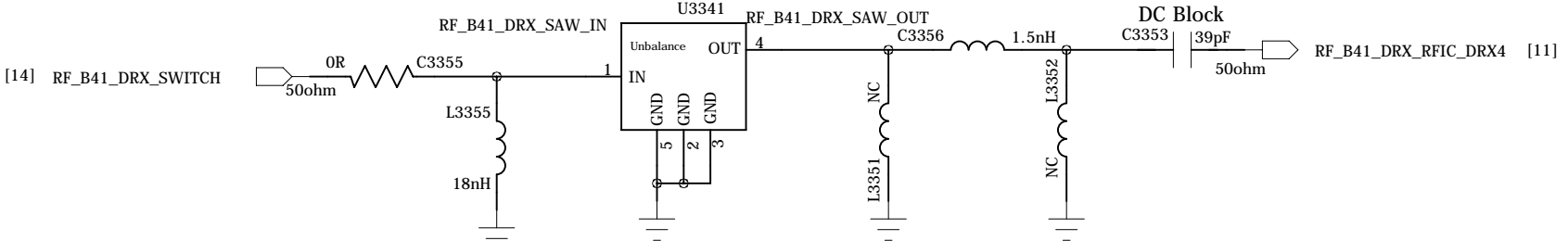
B20_B5_B5



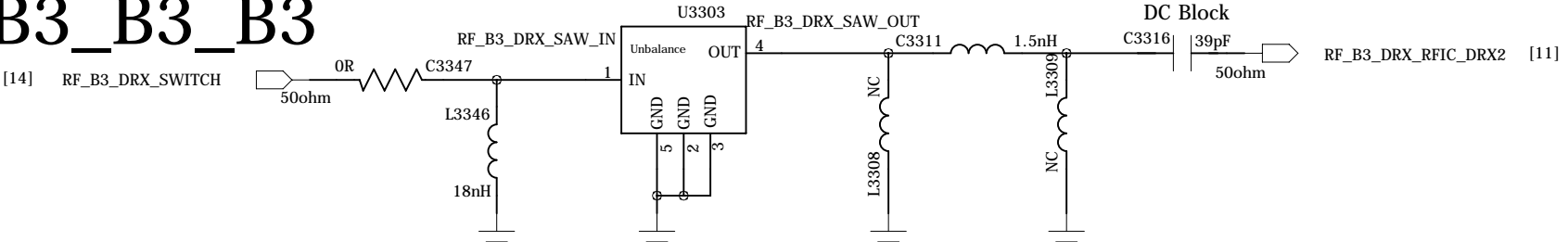
NC_NC_B28A



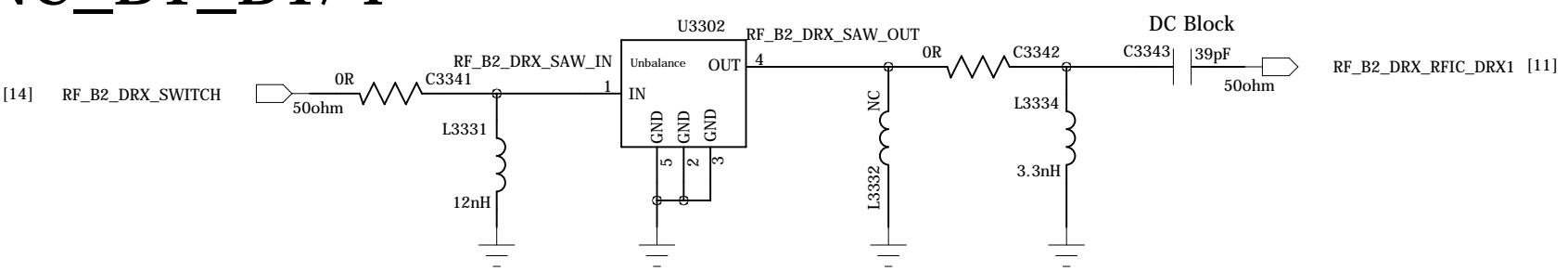
B7_B41_B7



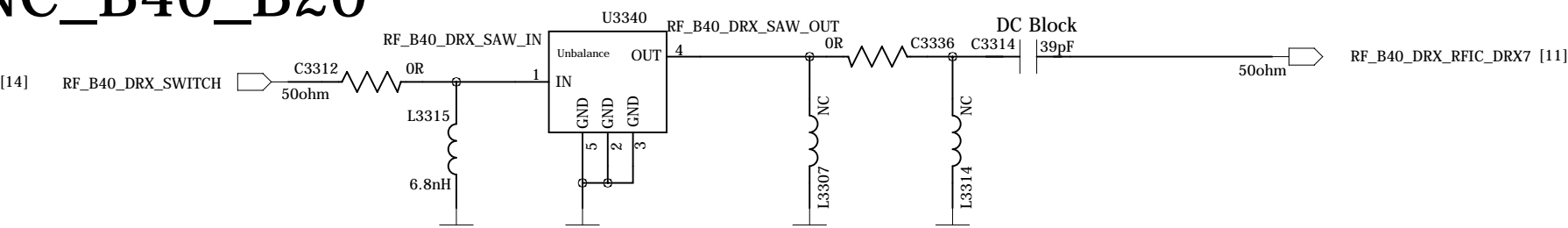
B3_B3_B3



NC_B1_B1/4



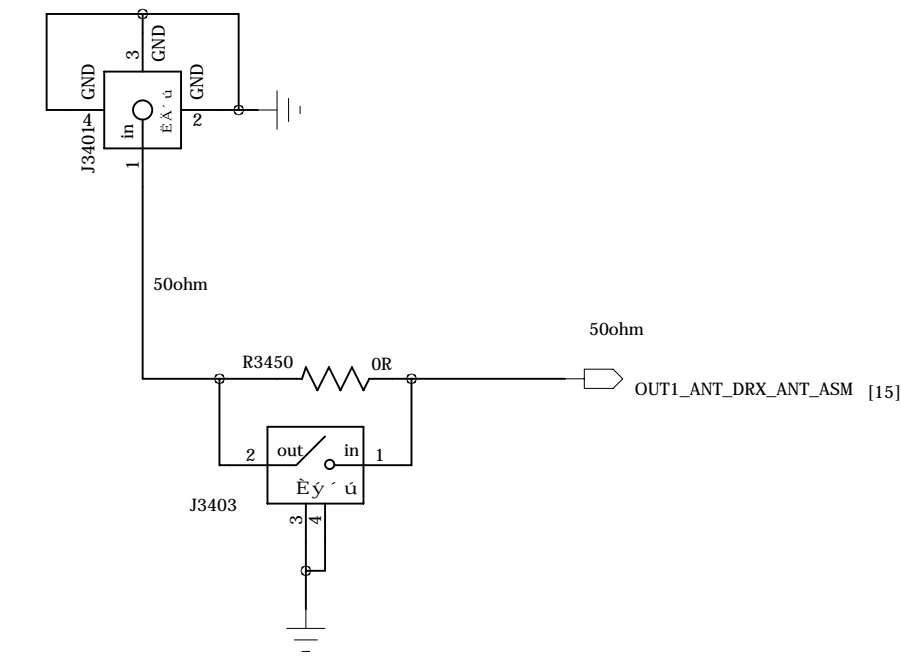
NC_B40_B20



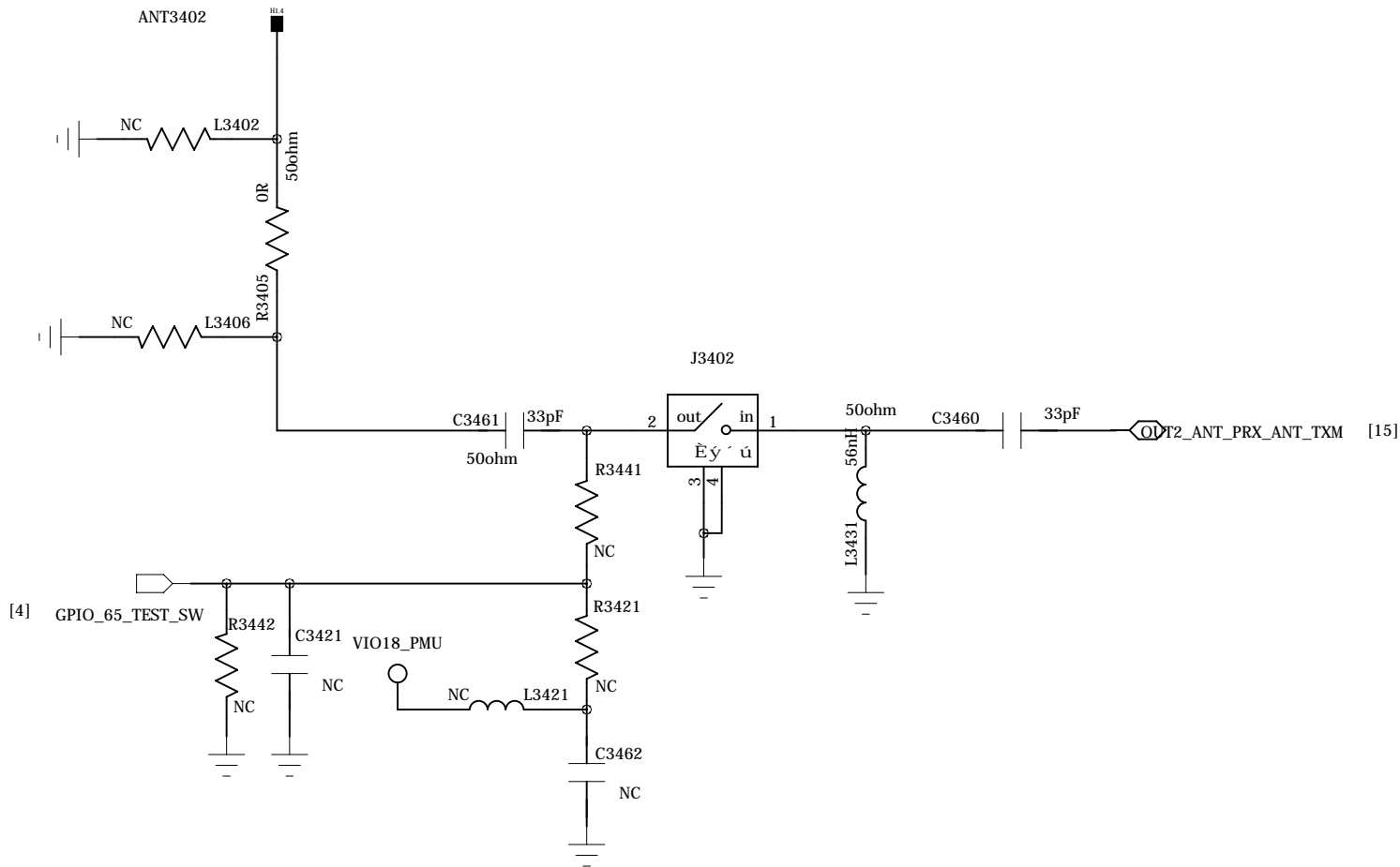
COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 33_RF_MT6177M_RF_DRX		VERSION: V1.0	SHEET: 14 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

RF_MT6177M_RF_ANT

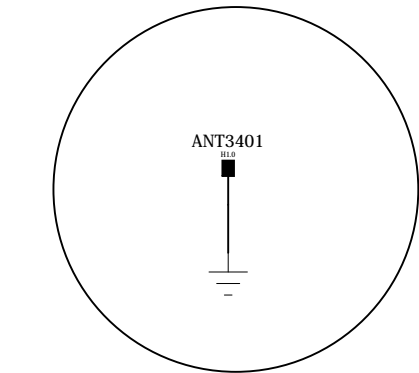
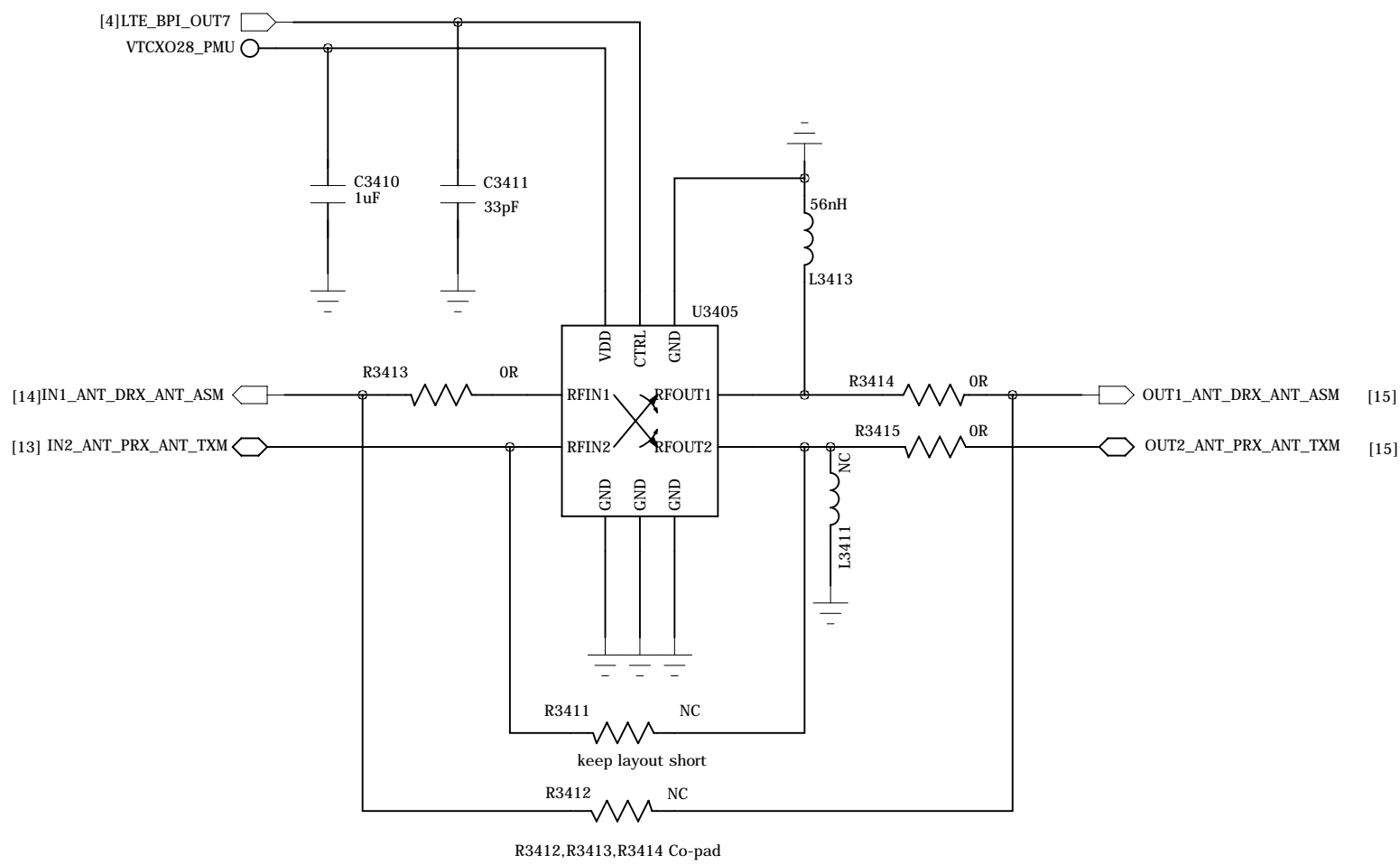
DRX ANT
791~821MHz + 1805~2690MHz



ASM_Main
791~960MHz + 1710~2690MHz

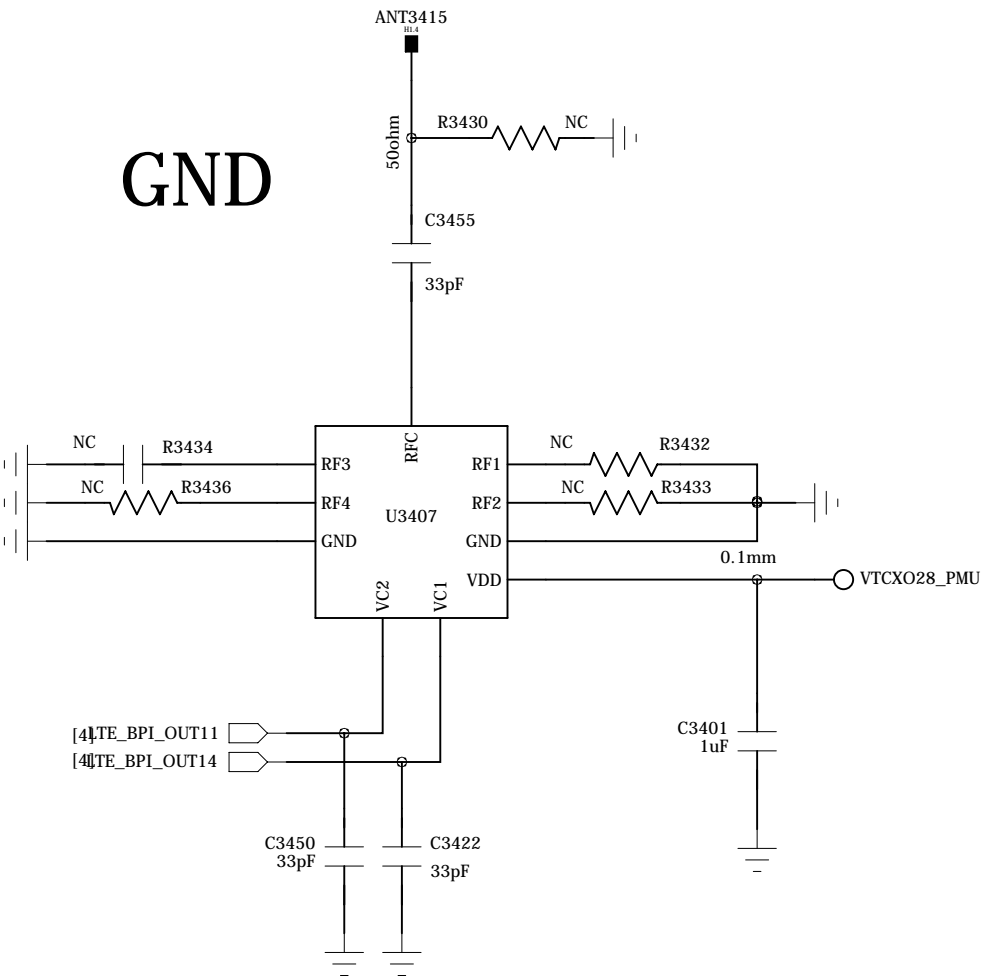


DPDT

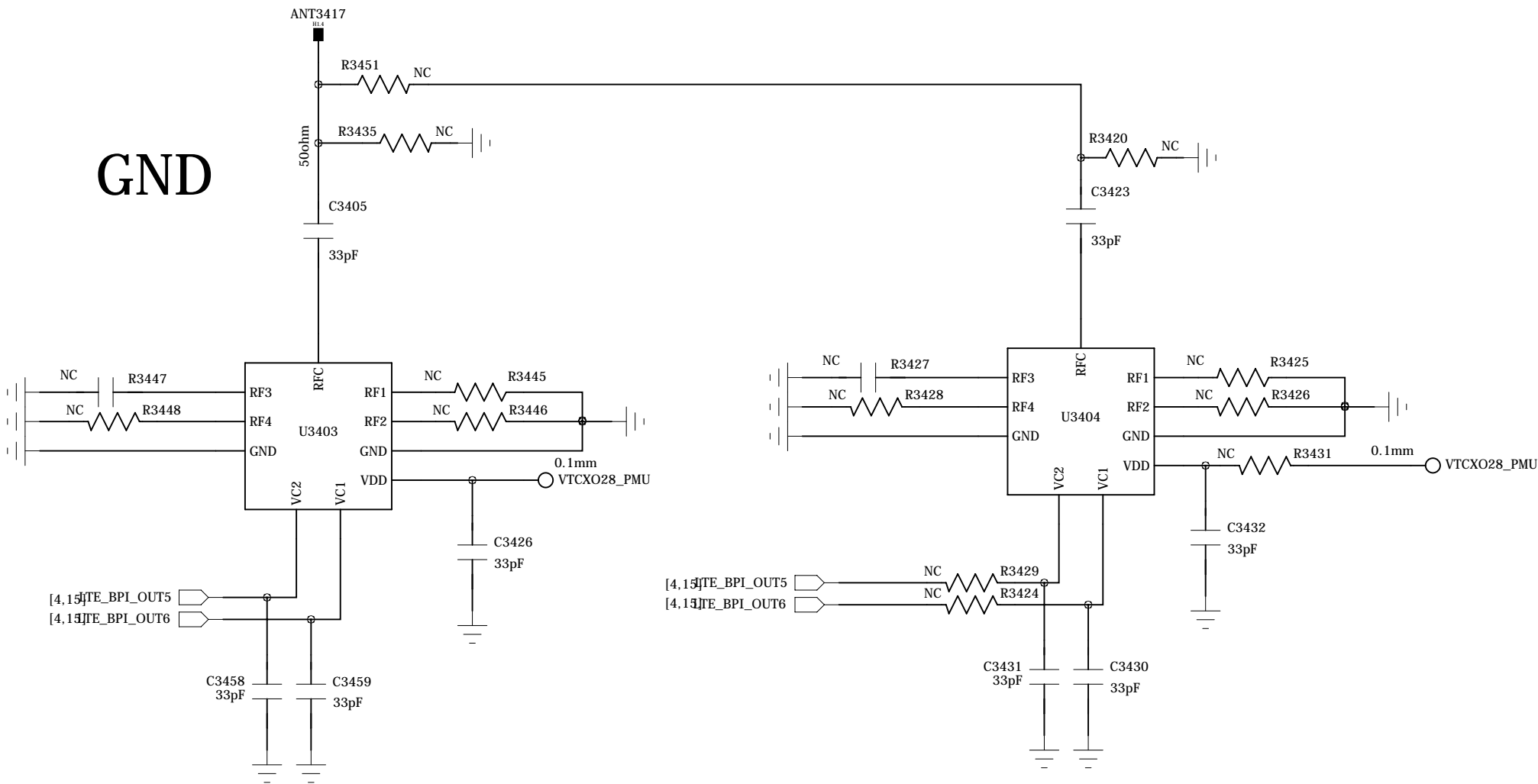


ANT switch

RF1119A & RF1694 control logic					
VC1	VC2	RF1	RF2	RF3	RF4
L	L	Y	N	N	N
L	H	N	Y	N	N
H	L	N	N	Y	N
H	H	N	N	N	Y



GND



COMPANY: TRANSSION HOLDINGS

MODEL: A6262

Modified Date: 2018/11/14

DRAWN

DJF/TS

DATED

20180809

TITLE: 34_RF_ANT_CONTROLLER

VERSION: V1.0

SHEET: 15 OF 24

CHECKED

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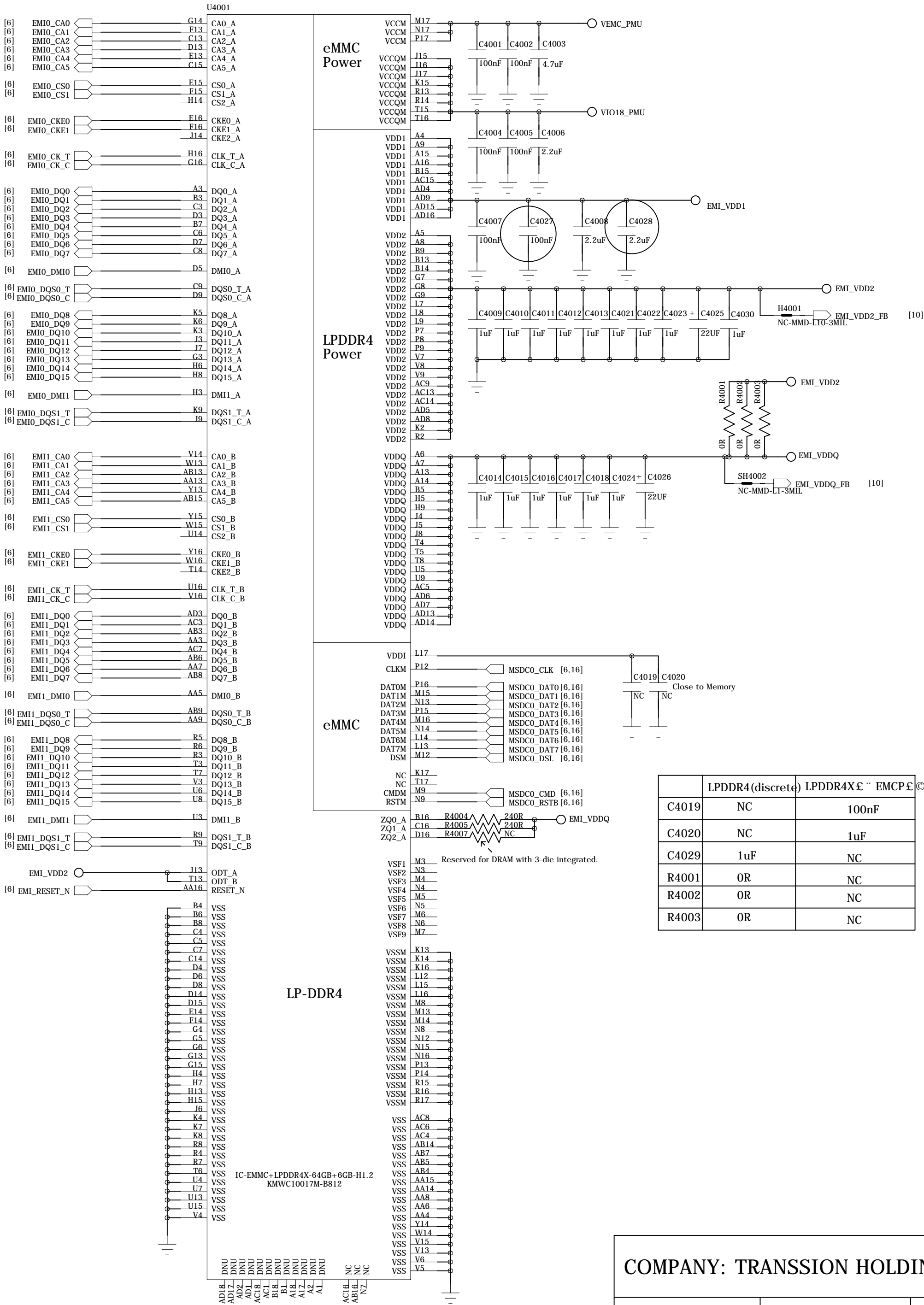
DATED

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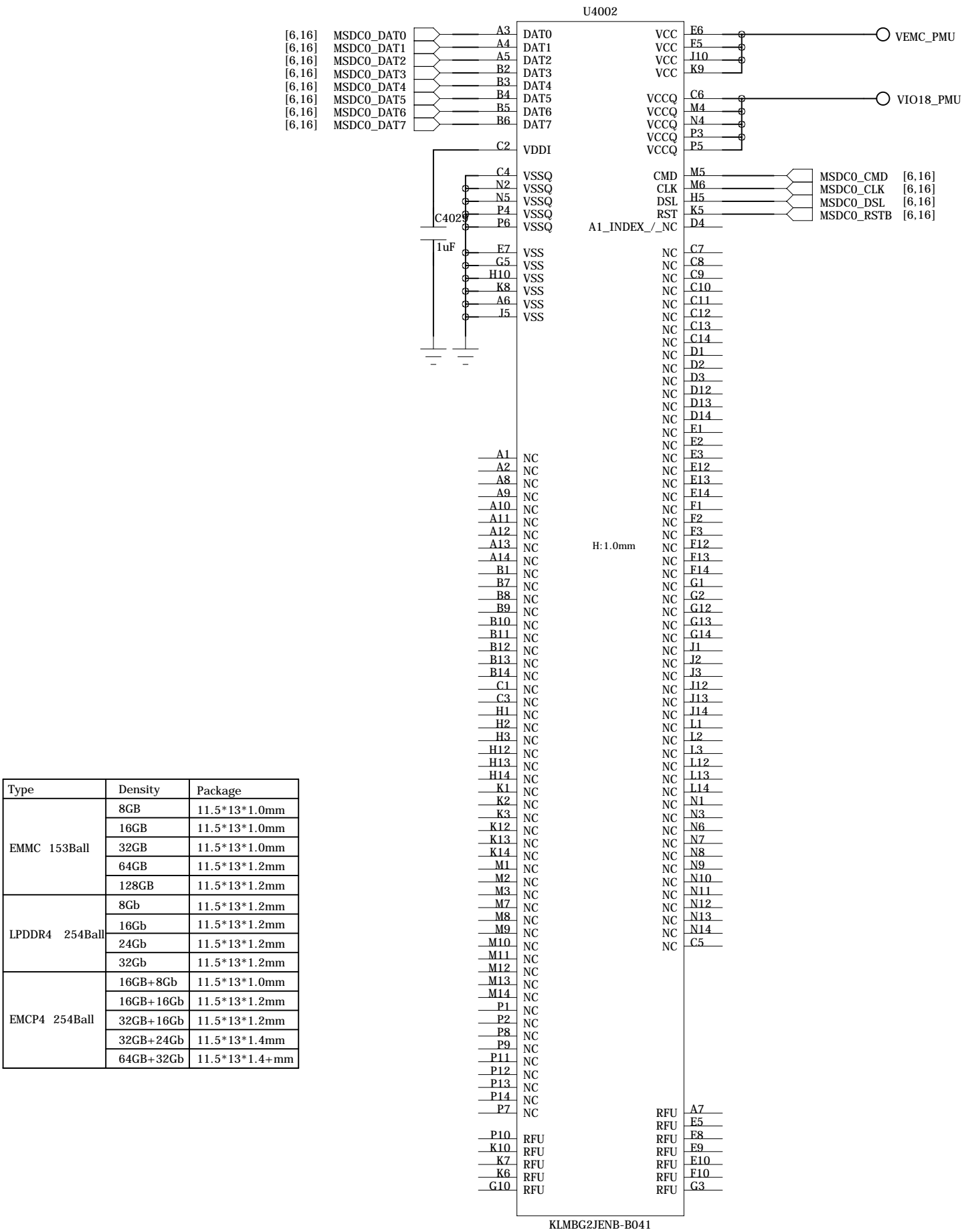
Confidentiality

CONFIDENTIAL

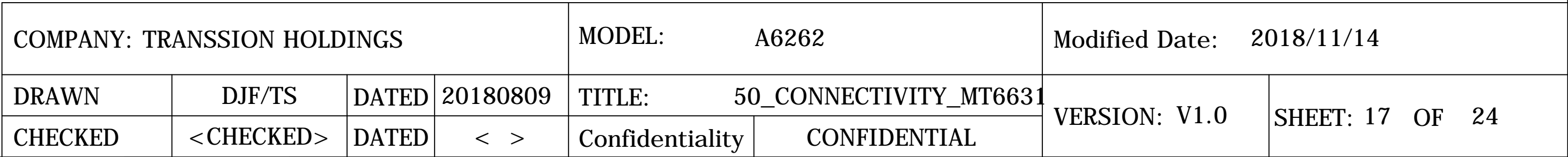
MEMORY_eMMC_LPDDR4X



MEMORY_eMMC

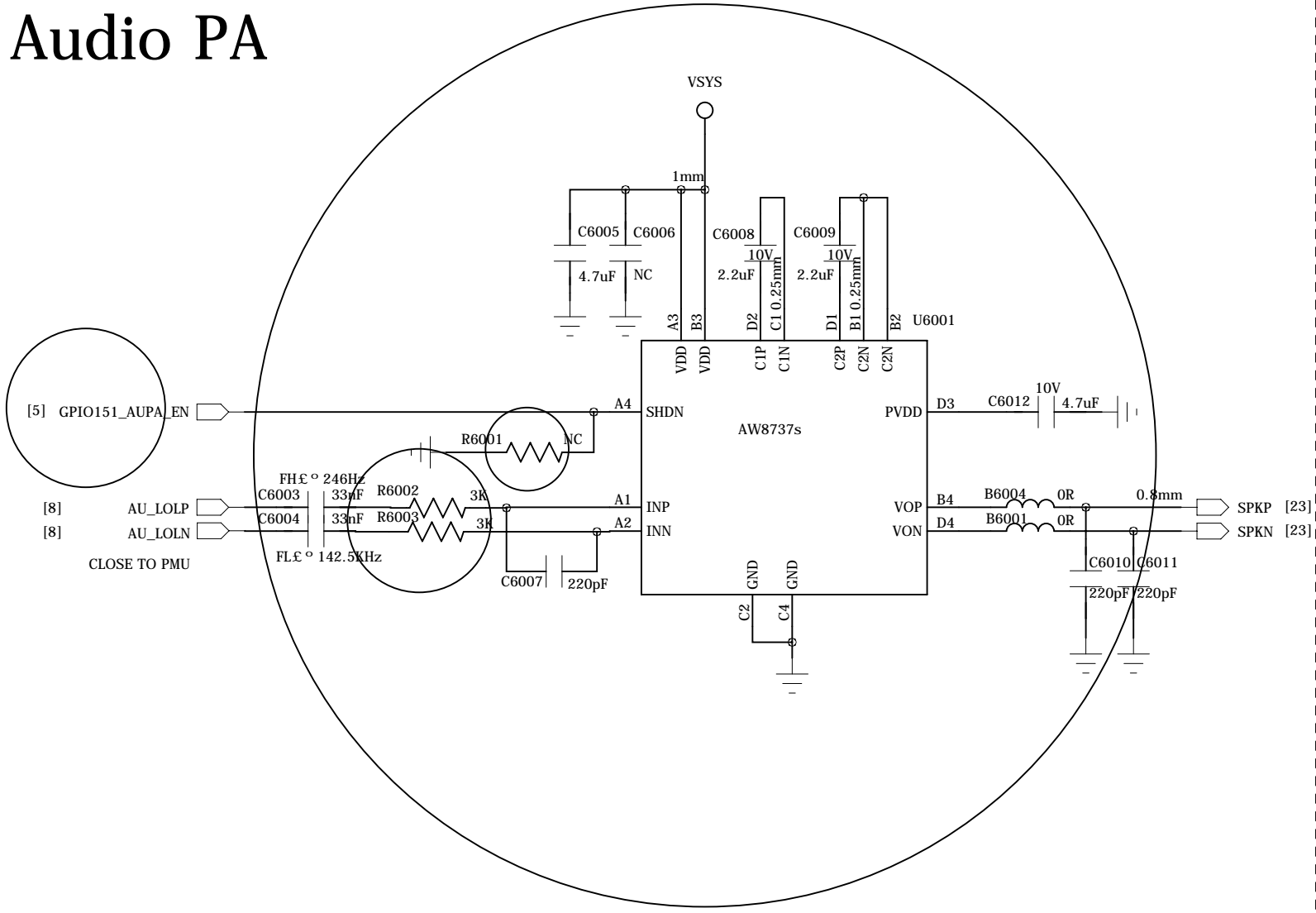


REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



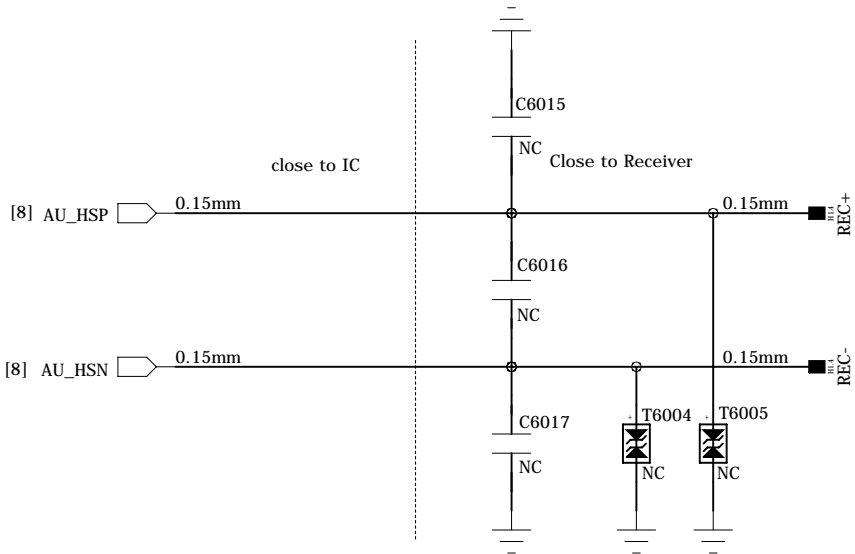
PERI_AUDIO_IO

Audio PA

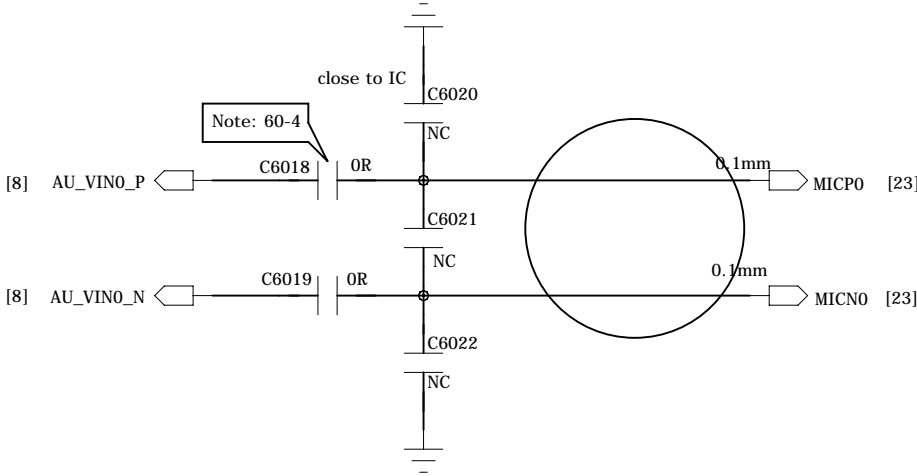
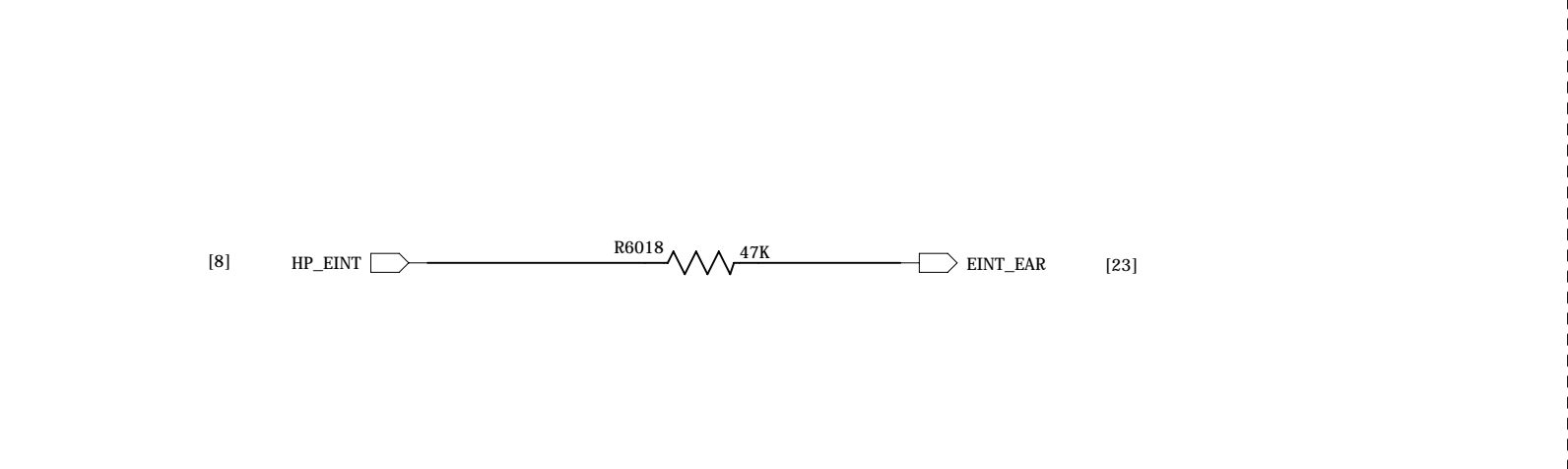


SPK

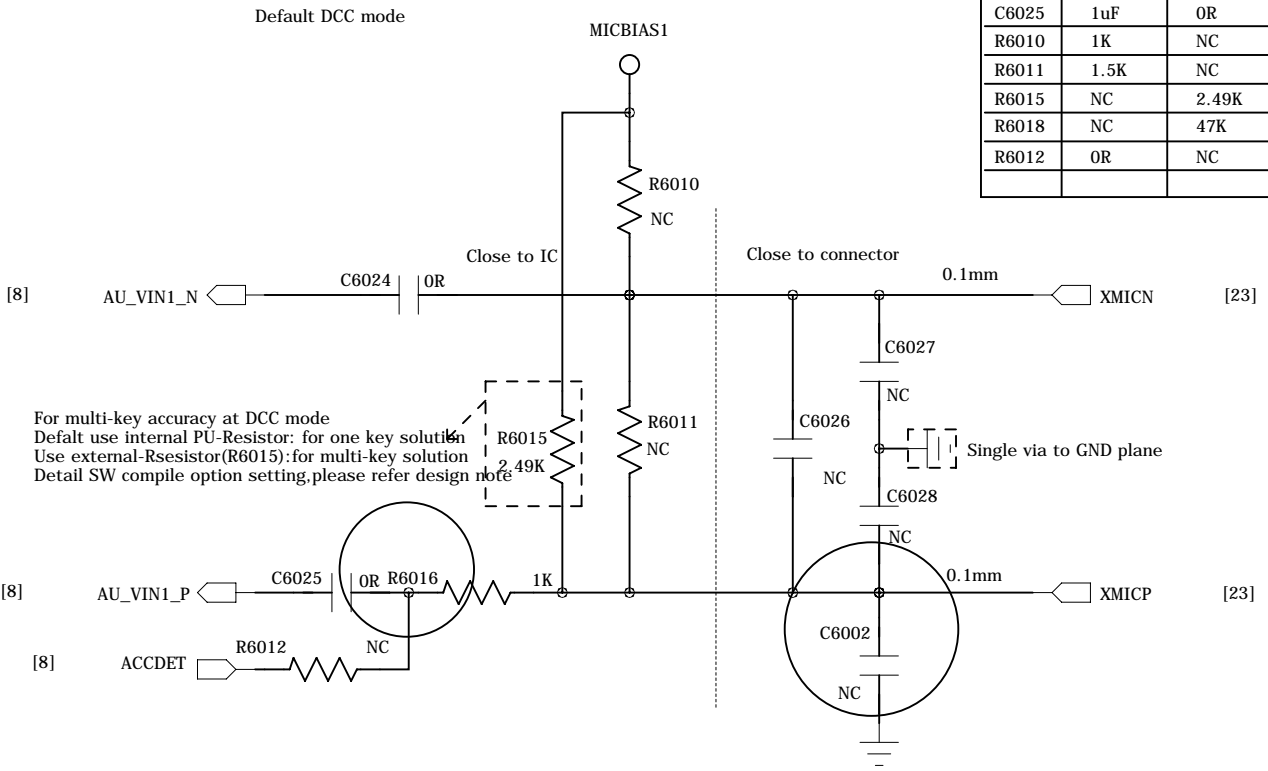
Receiver



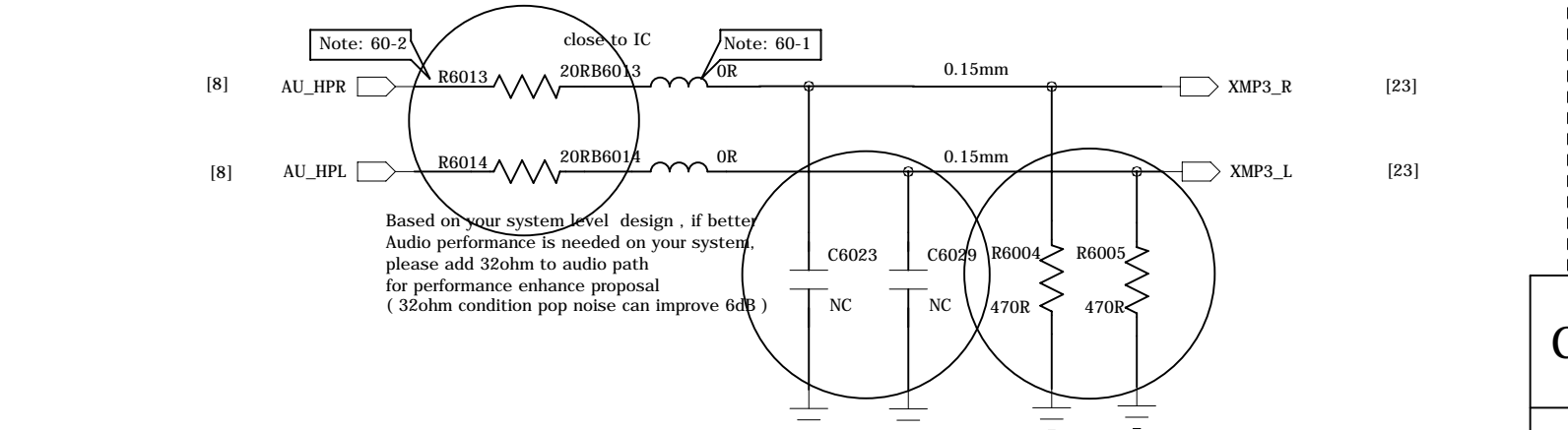
Main MICPHONE



Earphone MICPHONE



Earphone Receiver



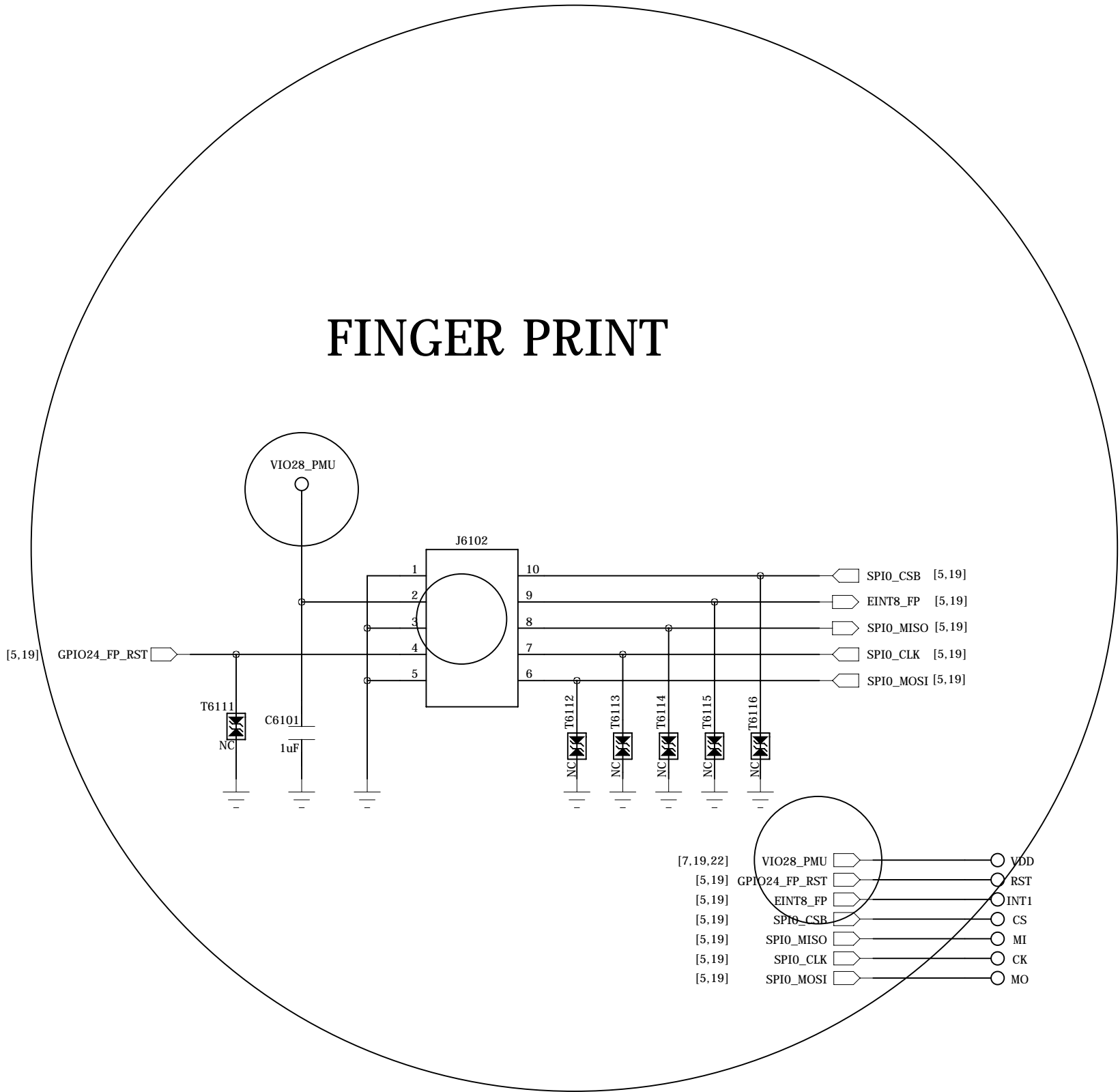
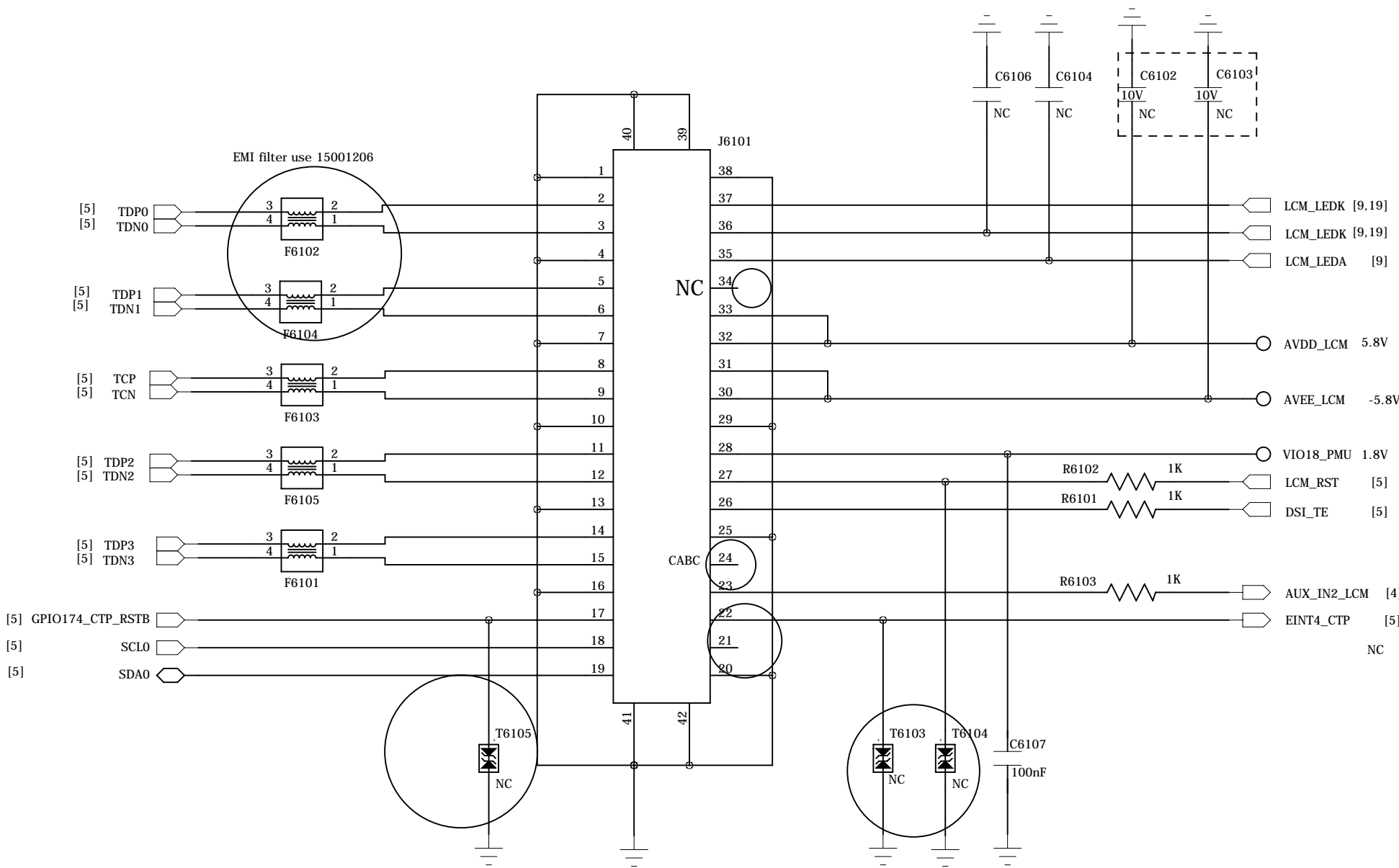
Schematic design notice of "60_PERI_AUDIO_IO" page.			
Note 60-1: B6009 B6010 B6013 B6014 needs change to "BLM18BD102SN1" for high THD performance(-90dB), but this BOM change will result in FM RSSI 10dB degraded.			
Note 60-2: To reserve a resistor in HPL and HPR in series connection both in order to optimize headphone pop noise. The recommended value of this resistor is 33R.			
Note 60-3: Layout trace from MT6353 ball J3 AUDREFN to Audio jack GND must surround shield with GND.			
Note 60-4: 0.1/1uF for ACC mode(1uF for WB_AMR Speech/0.1uF for NB_AMR Speech).OR for DCC mode.			

COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 60_PERI_AUDIO		VERSION: V1.0	SHEET: 18 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

LCD_CTP_INTERFACE 6.19HD+

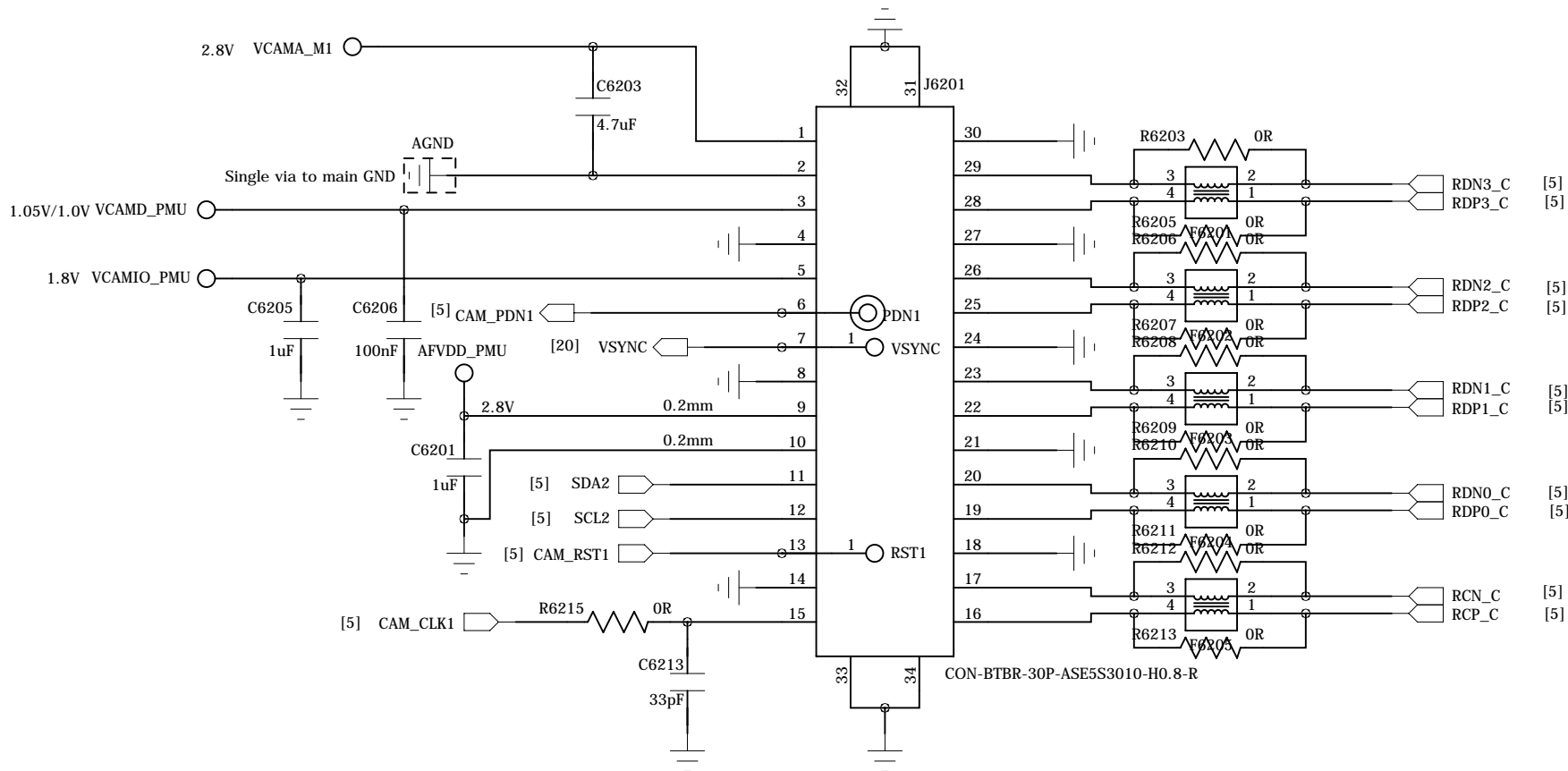
CTP (NT35672) I2C address: 0X01 (Write:0x02, Read:0x03)



COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 61_PERI_LCM_CTP_FP		VERSION: V1.0	SHEET: 19 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

PERI_REAR_CAMERA

REAR CAMERA Wide (13M/16M+ AF)



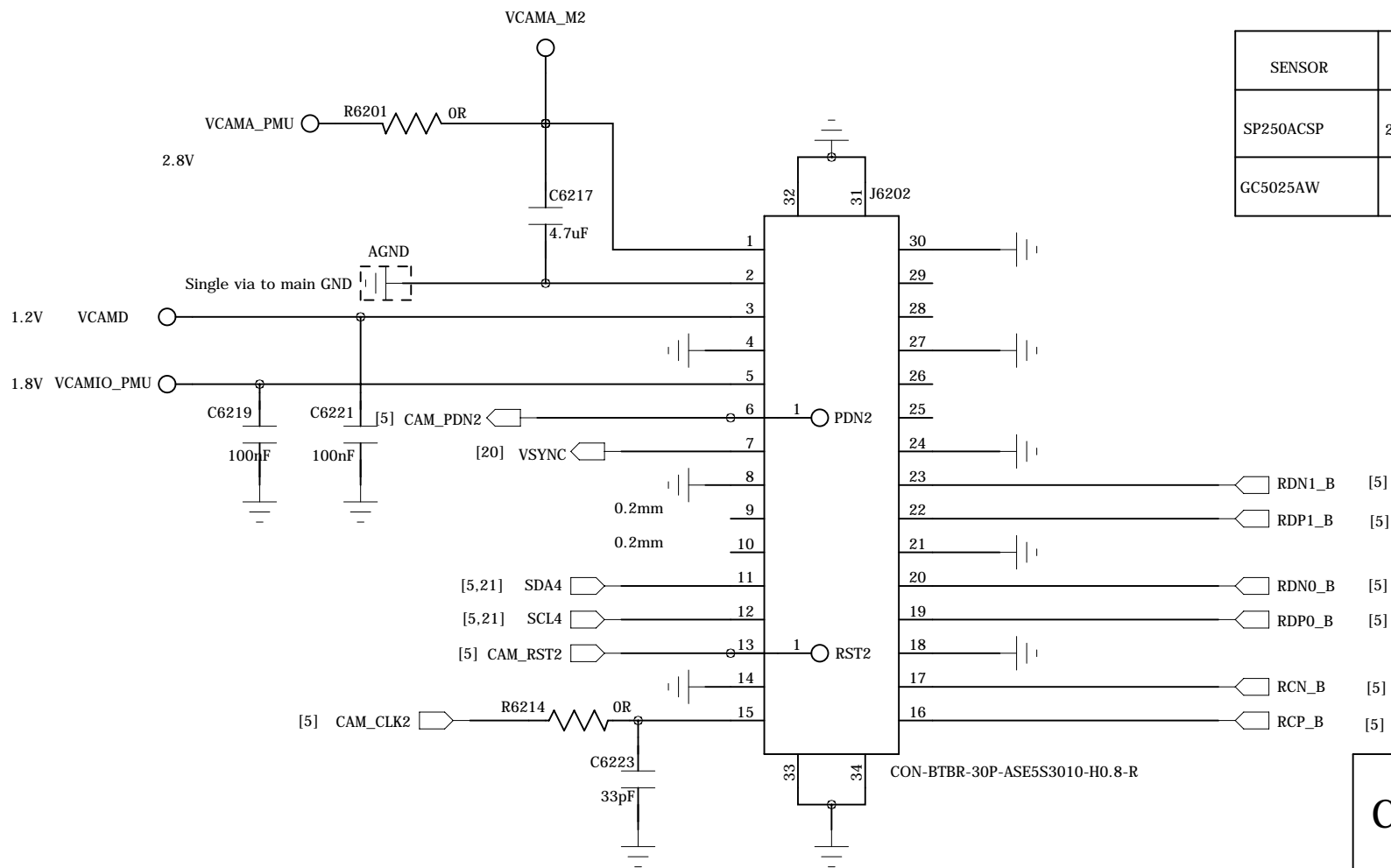
SENSOR	VCAMA	VCAMD	VCAMIO	AFVDD
SSK3L6XX03-FGX9	2.8V 60mA	1.05V 200mA	1.8V 1mA	2.8V 100mA
S5K2P7SQ03-FGX9	2.8V 46mA	1.0V 170mA	1.8V 0.5mA	2.8V 100mA
GC8034W	2.8V 35mA	1.2V 140mA	1.8V 10mA	2.8V 120mA

		SENSOR	I2C Address
8M		SENSOR(GC8034W) MOTOR(DW9714P)	Write:0x6E Read:0x6F Write:0x18 Read:0x19
16M		SENSOR(S5K2P7SQ03-FGX9) MOTOR(DW9718S) EEPROM(BL24SA64-CS)	Write:0x20 Read:0x21 Write:0x18 Read:0x19 Write:0xA0 Read:0xA1
13M	Sunvin	SENSOR(S5K3L6XX03-FGX9) MOTOR(DW9718S) EEPROM(P24C64E-C4H-MIR)	Write:0x20 Read:0x21 Write:0x18 Read:0x19 Write:0xA0 Read:0xA1
	Truly	SENSOR(S5K3L6XX03-FGX9) MOTOR(DW9718S)	Write:0x20 Read:0x21 Write:0x18 Read:0x19
	SHINETECH	EEPROM(BL24SA64-CS)	Write:0xA0 Read:0xA1

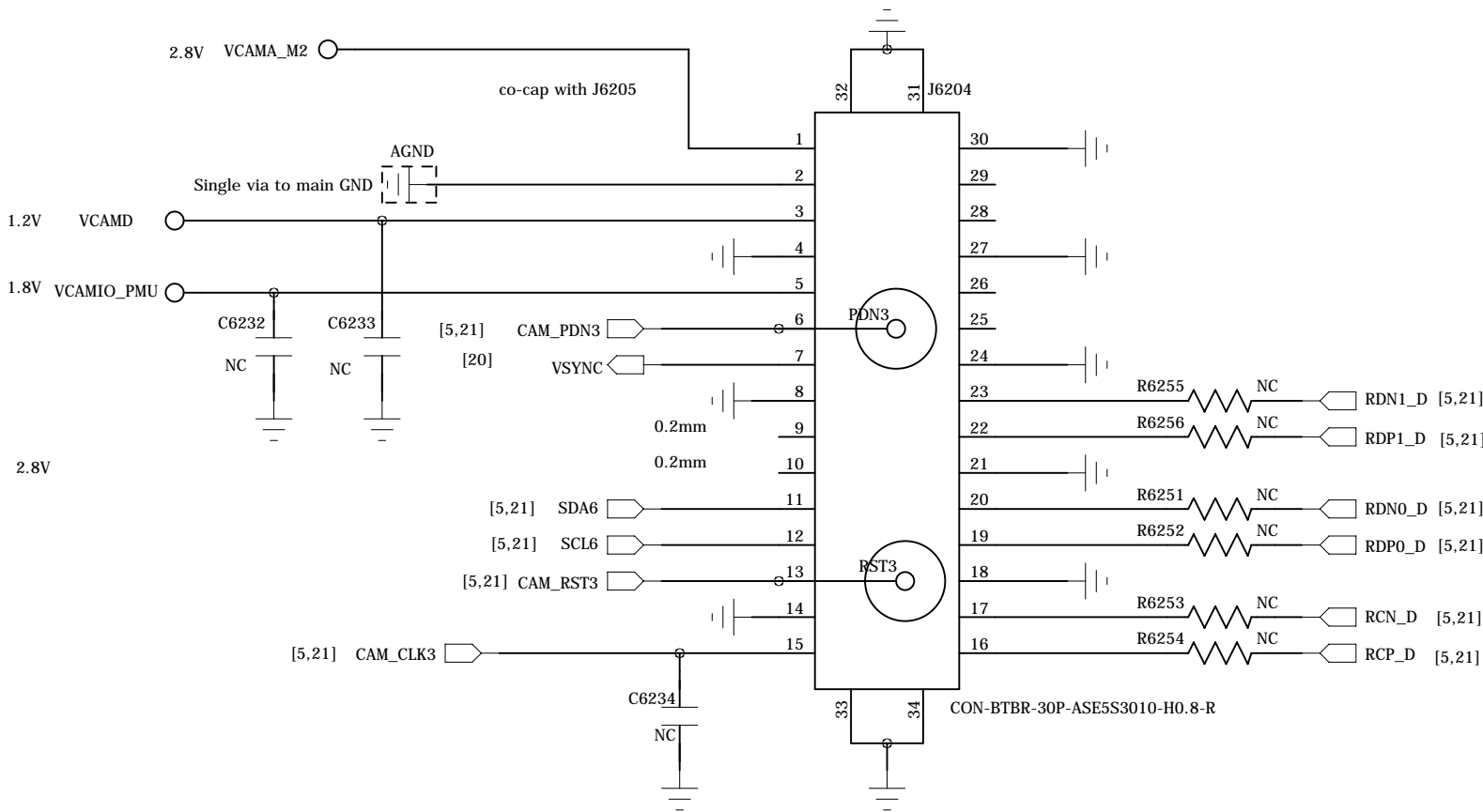
SENSOR	VCAMA	VCAMD	VCAMIO		I2C Address
GC8034W	2.8V 35mA	1.2V 140mA	1.8V 10mA		Write:0x6E Read:0x6F

REAR CAMERA Tele (8M+ FF)

REAR CAMERA Black&White (2M/5M+ FF)



SENSOR	VCAMA	VCAMD	VCAMIO	I2C Address
SP250ACSP	2.8V 30mA type	NC	1.8V 18mA type	Write:0x78 Read:0x79
GC5025AW	2.8V 30mA	1.2V 60mA	1.8V 2mA	Write:0x6E Read:0x6F

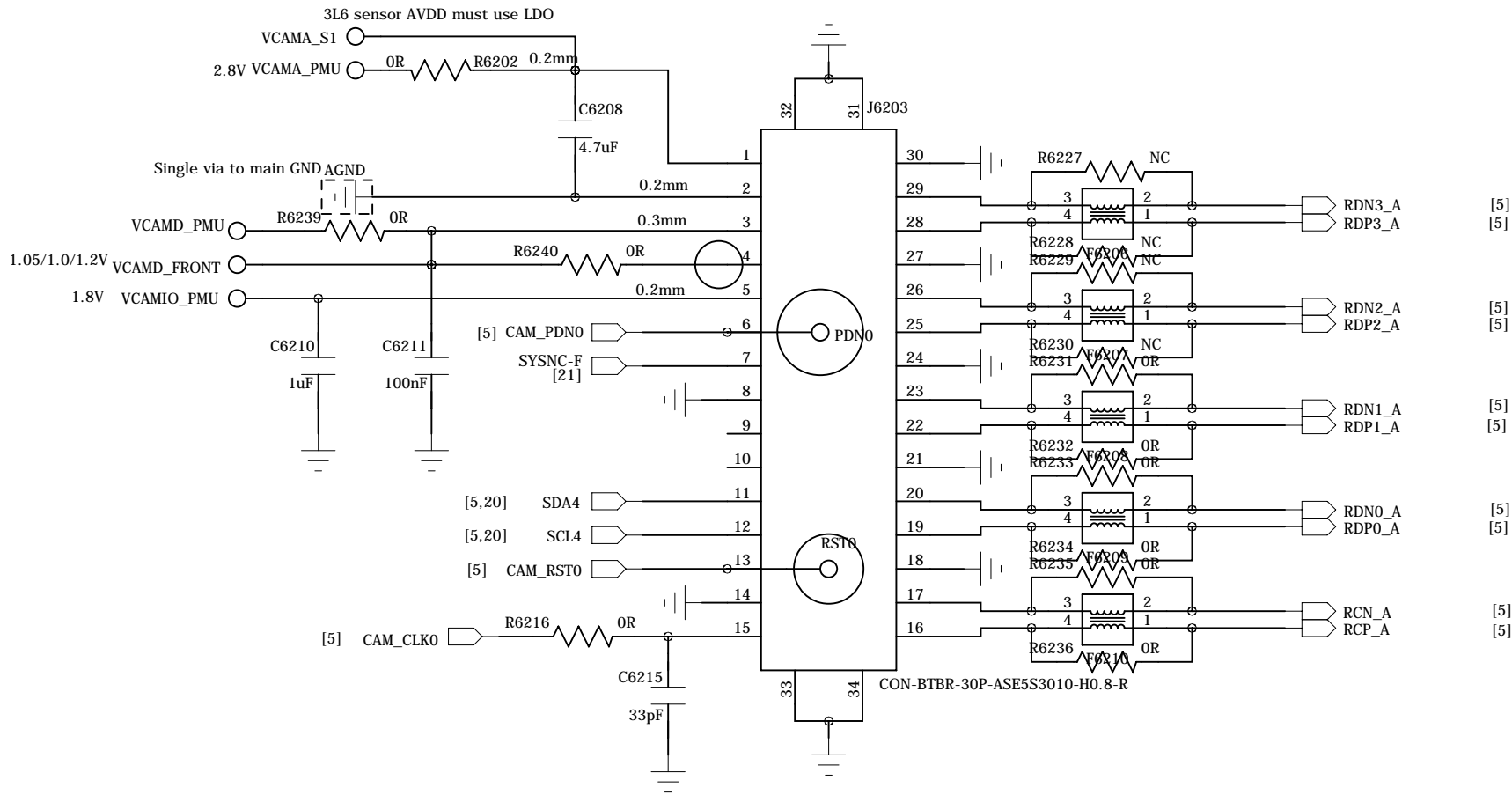


COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 62_PERI_REAR_CAMERA		VERSION: V1.0	SHEET: 20 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

PERI_FRONT_CAMERA

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

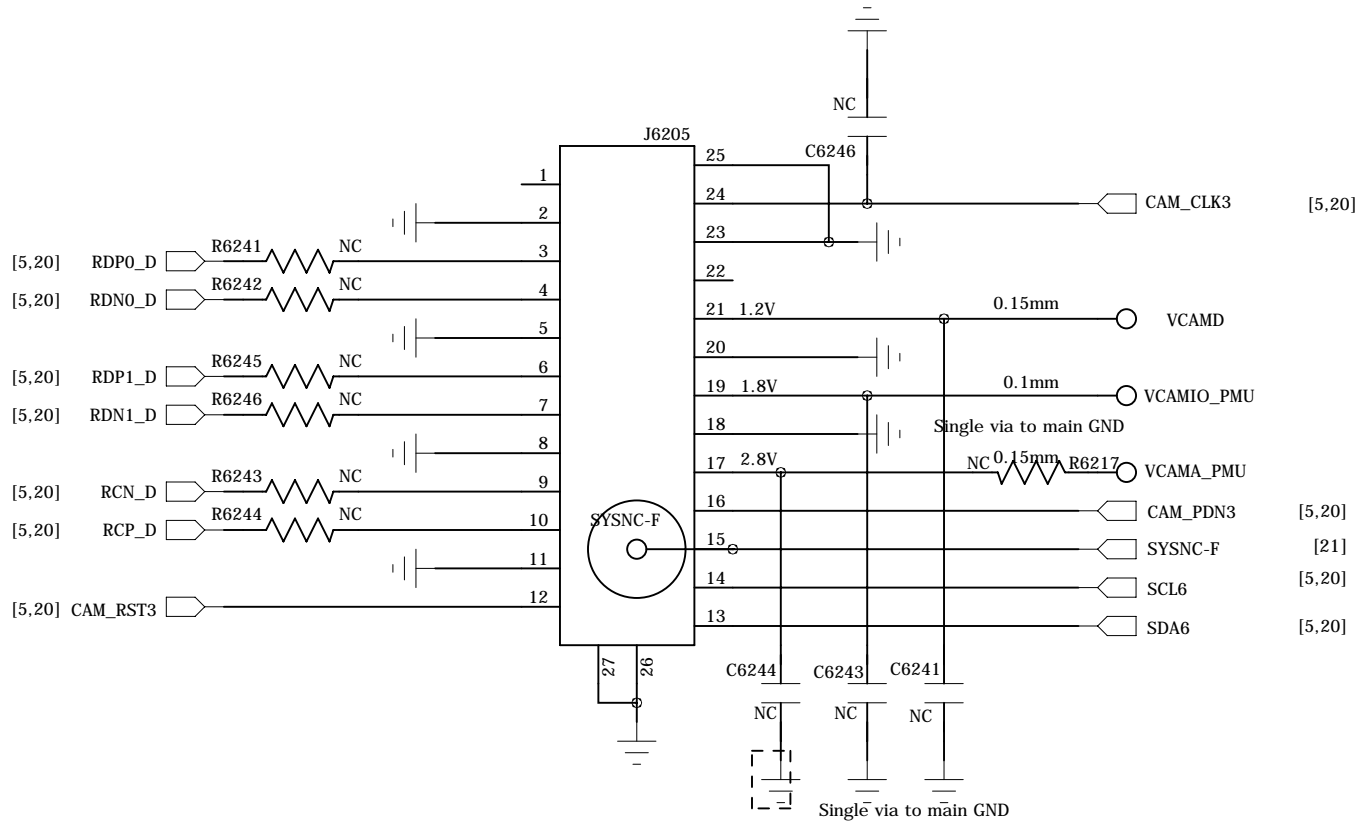
FRONT MAIN CAMERA (MAX 32M+ FF)



	SENSOR	I2C Address
8M	GC8C34W	Write:0x6C Read:0x6D
13M	S5K3L6XX03-FGX9 P24C64E-C4H-MIR	Write:0x5A Read:0x5B Write:0xA2 Read:0xA3
16M	S5K3P9SP04-FGX9 BL24SA64-CS	Write:0x5A Read:0x5B Write:0xA2 Read:0xA3
20M	S5K2T7SP04-FGX9 FM24C64D-CT-T-G-A0	Write:0x5A Read:0x5B Write:0xA2 Read:0xA3
24M Ofilm	S5K2X7SP03-FGX9 FM24C64D-CT-T-G-A0	Write:0x5A Read:0x5B Write:0xA2 Read:0xA3
24M SUNNY	S5K2X7SP03-FGX9 BL24SA64	Write:0x5A Read:0x5B Write:0xA0 Read:0xA1
32M	S5KGD1SP FM24C64D-CT-T-G-A0	Write:0x5A Read:0x5B Write:0xA2 Read:0xA3

SENSOR	VCAMA	VCAMD	VCAMIO
GC8C34W	2.8V 35mA	1.2V 140mA	1.8V 10mA
S5K3L6XX03-FGX9	2.8V 60mA	1.05V 200mA	1.8V 1mA
S5K3P9SP04-FGX9	2.8V 36.24mA	1.05V 112.3mA	1.8V 0.97mA
S5K2T7SP04-FGX9	2.8V 60mA	1.0V 220mA	1.8V 5mA
S5K2X7SP03-FGX9	2.8V 60mA	1.05V 245mA	1.8V 15.6mA
S5KGD1SP	2.8V 60mA	1.05V 245mA	1.8V 15.6mA

FRONT SUB CAMERA (VGA/2M+FF)
Reserve for FRONT main CAMERA (8M+FF)



one front 8M camera use J6205

SENSOR	VCAMA	VCAMD	VCAMIO	I2C Address
SP0A09	2.8V ??	1.2V ??	1.8V ??	Write:0x42 Read:0x43
SP250A	2.8V 18mA	NC	1.8V 30mA	Write:0x78 Read:0x79
GC8C34W(MAIN)	2.8V 35mA	1.2V 140mA	1.8V 10mA	Write:0x6C Read:0x6D

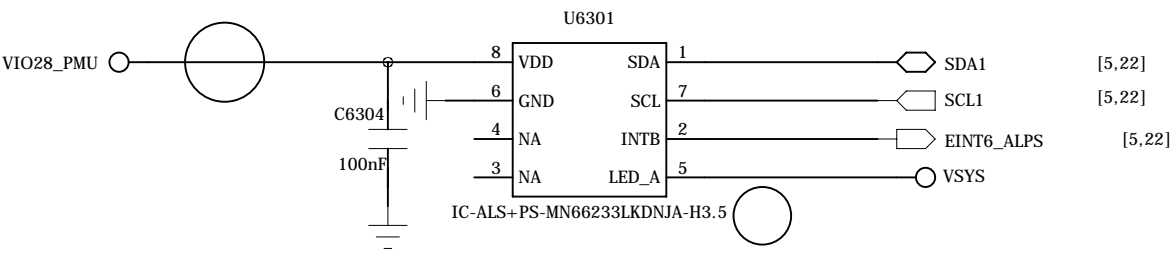
COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 62_PERI_FRONT_CAMERA		VERSION: V1.0	SHEET: 21 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

PERI_SENSORS

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

Notch
AL& PS Sensor

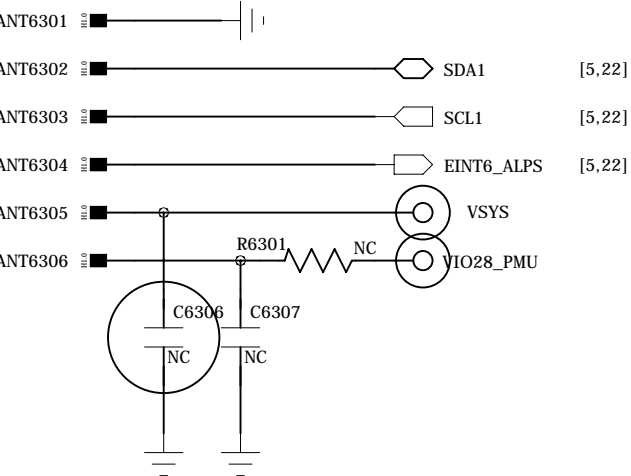
MN28233LKDN: I2C address: Write:0x92, Read:0x93



	Notch LCM	drop
ALS&PS	MN28233LKDN	STK3338 + CM32183A3OG

STK3332--ALPS

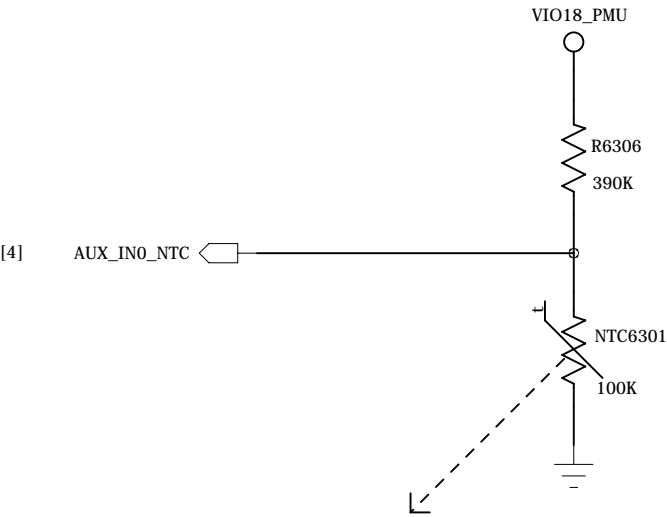
STK3332: I2C address: Write:0x8E, Read:0x8F



CM32183A3OG--ALS

CM32183A3OG: I2C address: Write:0x52, Read:0x53

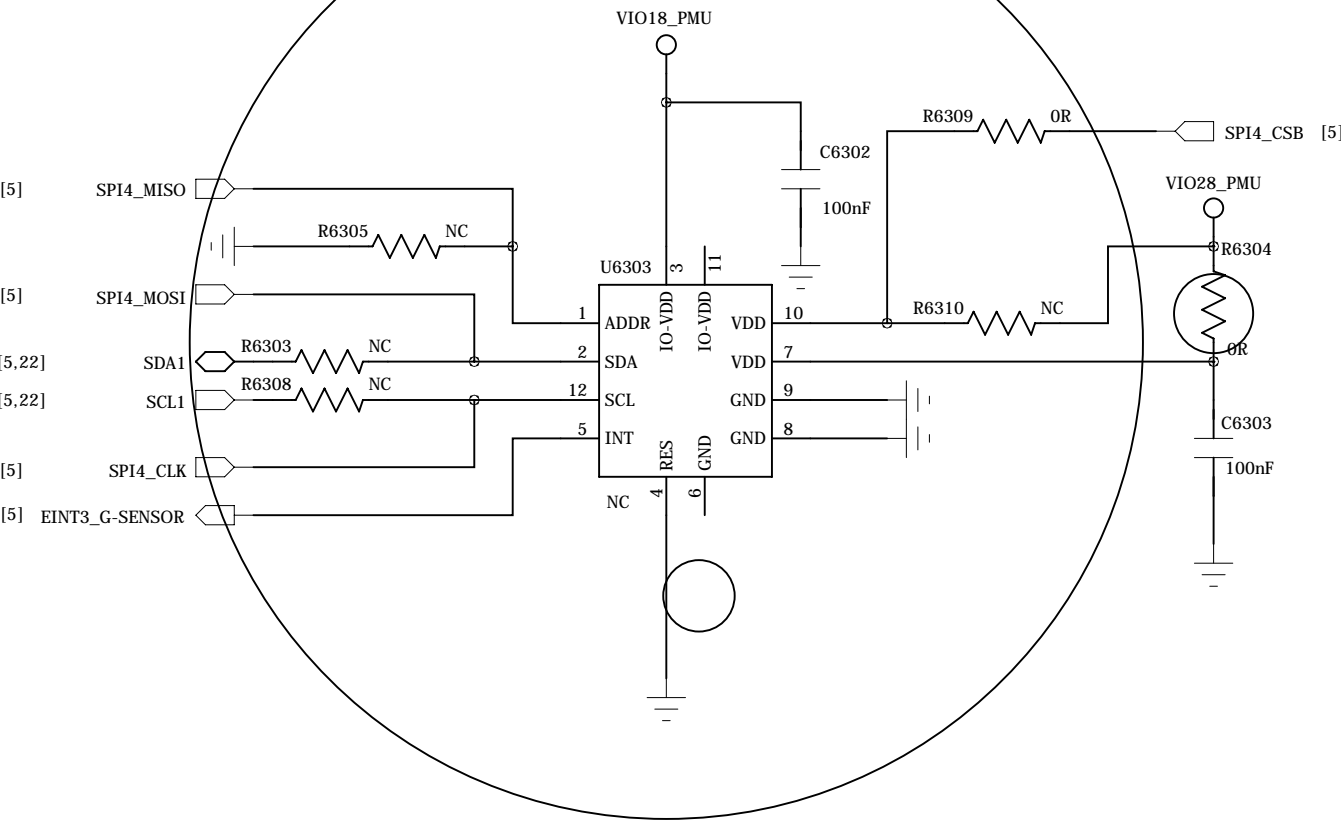
Thermistor to sense AP
temperature



1. NTC6301 must keep a distance about 6~8 mm away from AP and far from other heat sources 10 mm at least.
2. The distance is the shortest distance from package edge to edge.

G-Sensor

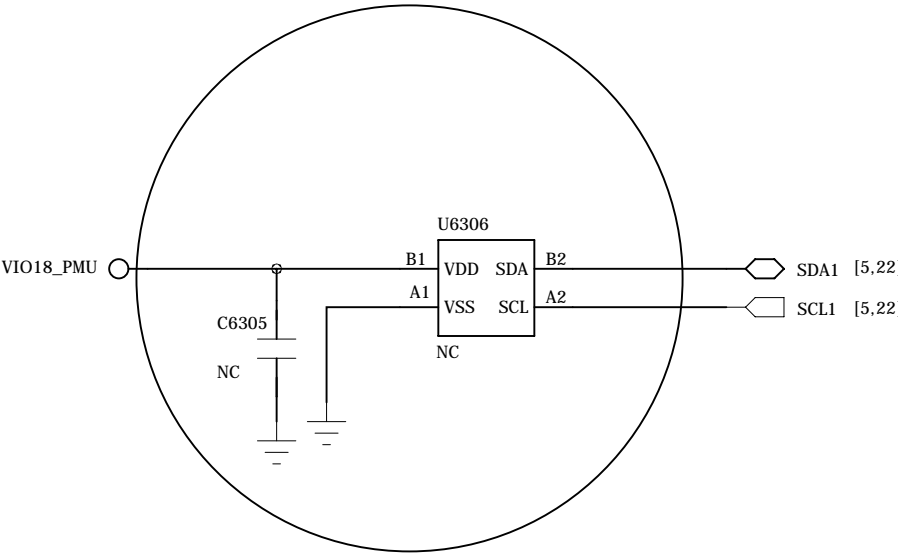
DA218-B: ADDRESS: 0x4E (Write) / 0x4F (Read)
MC3416-P: ADDRESS: 0x98 (Write) / 0x99 (Read)



component	DA218-B	MC3416-P	STK8321	E2DM
C6302	100nF	100nF	1uF	100nF
R6304	OR	NC	OR	OR
C6303	100nF	NC	1uF	100nF

M-Sensor (COMPASS)

AK09918C-L: I2C ADDRESS: 0x18 (Write) / 0x19 (Read)

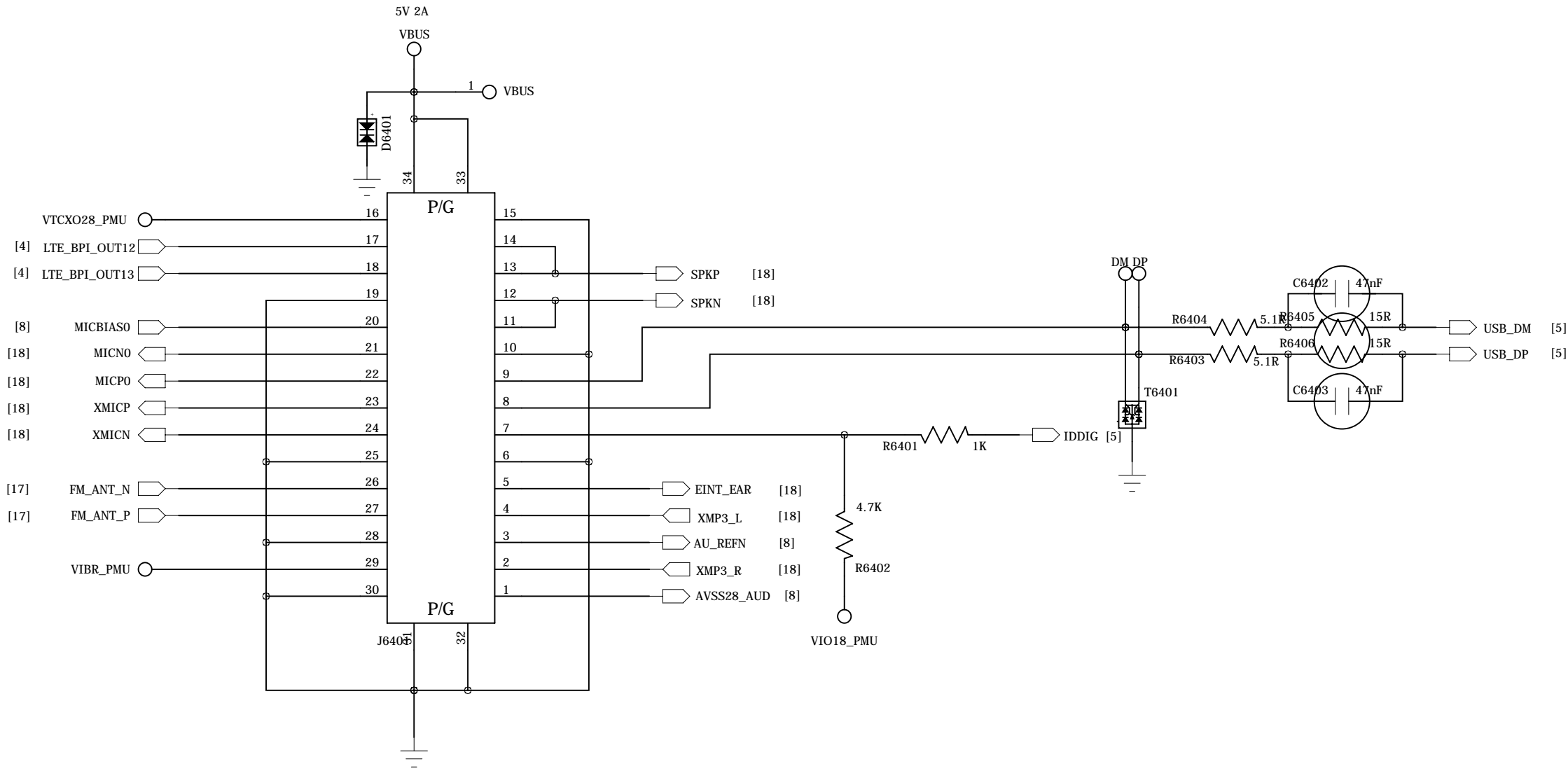


Unipolar HALL

COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 63_PERI_SENSORS		VERSION: V1.0	SHEET: 22 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

PERI_IO_VIB

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

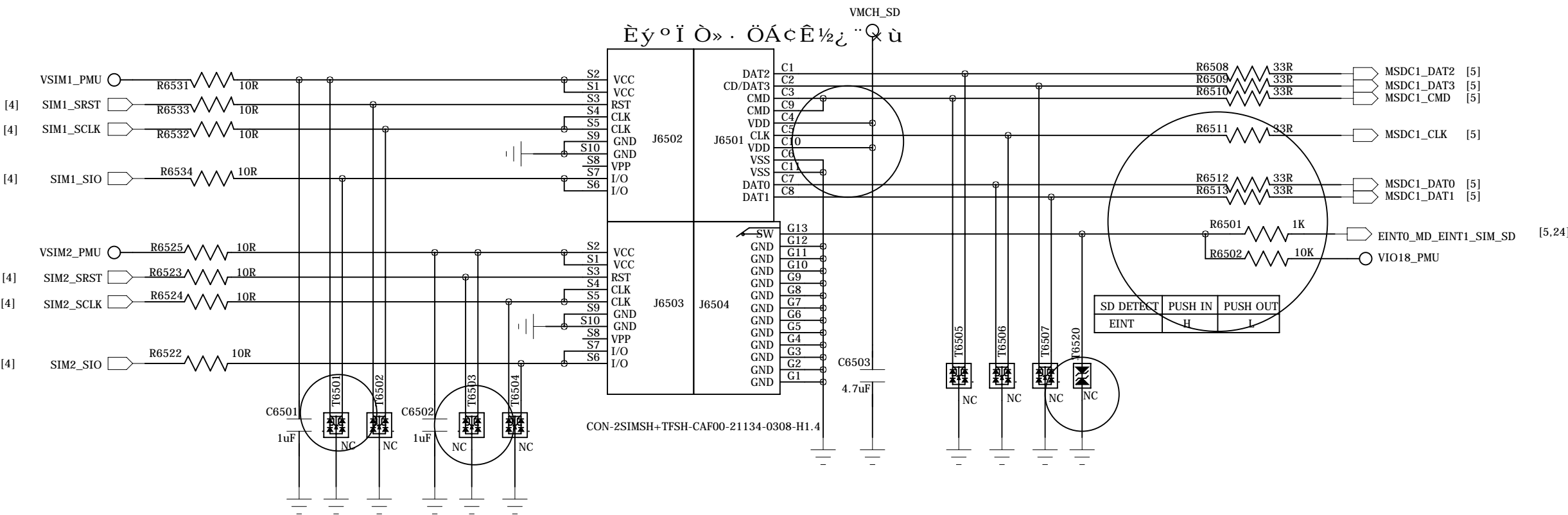


COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 64_PERI_IO_VIB		VERSION: V1.0	SHEET: 23 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		

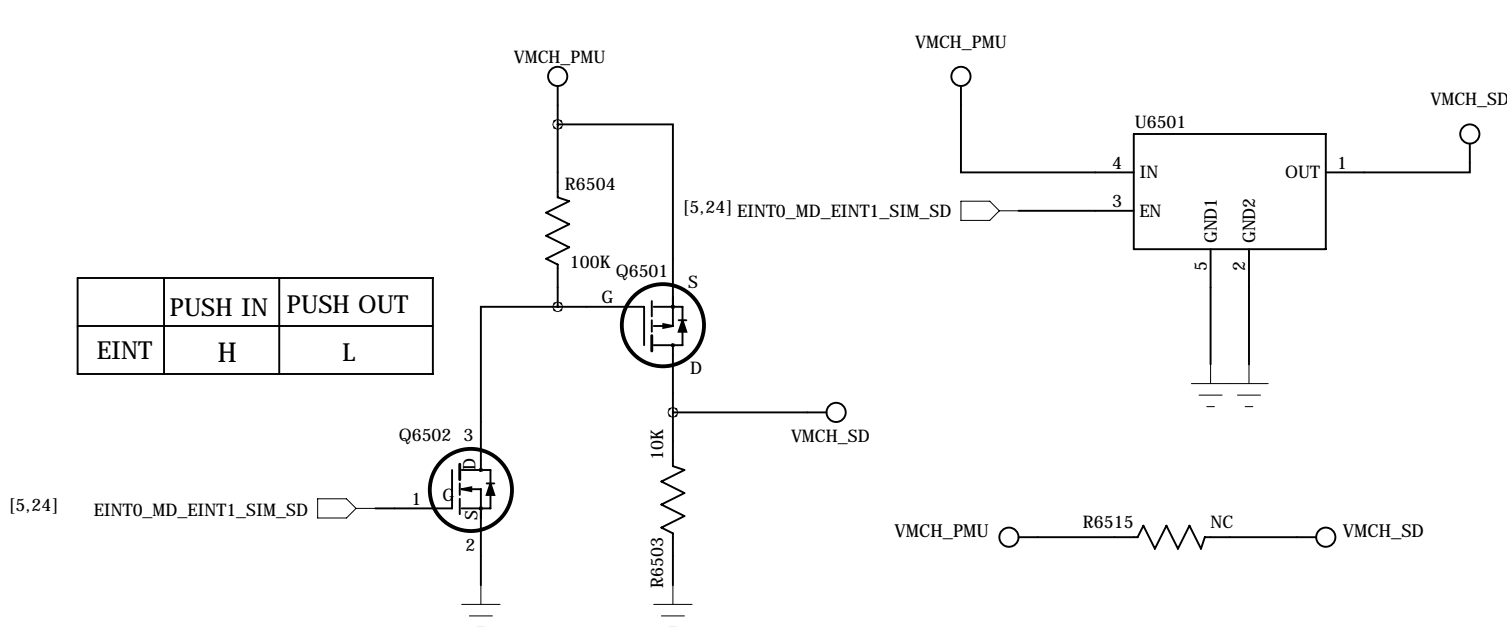
PERI_SIM_SD_LED

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:

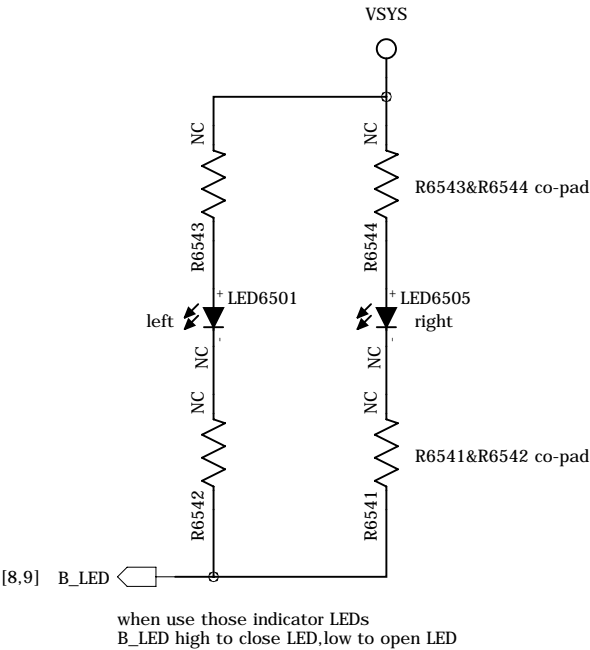
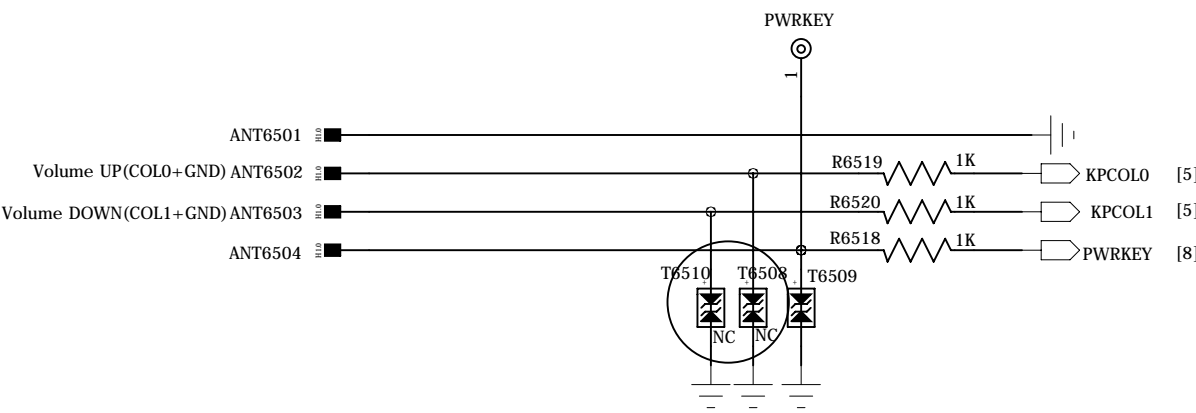
SIM/SD CARD



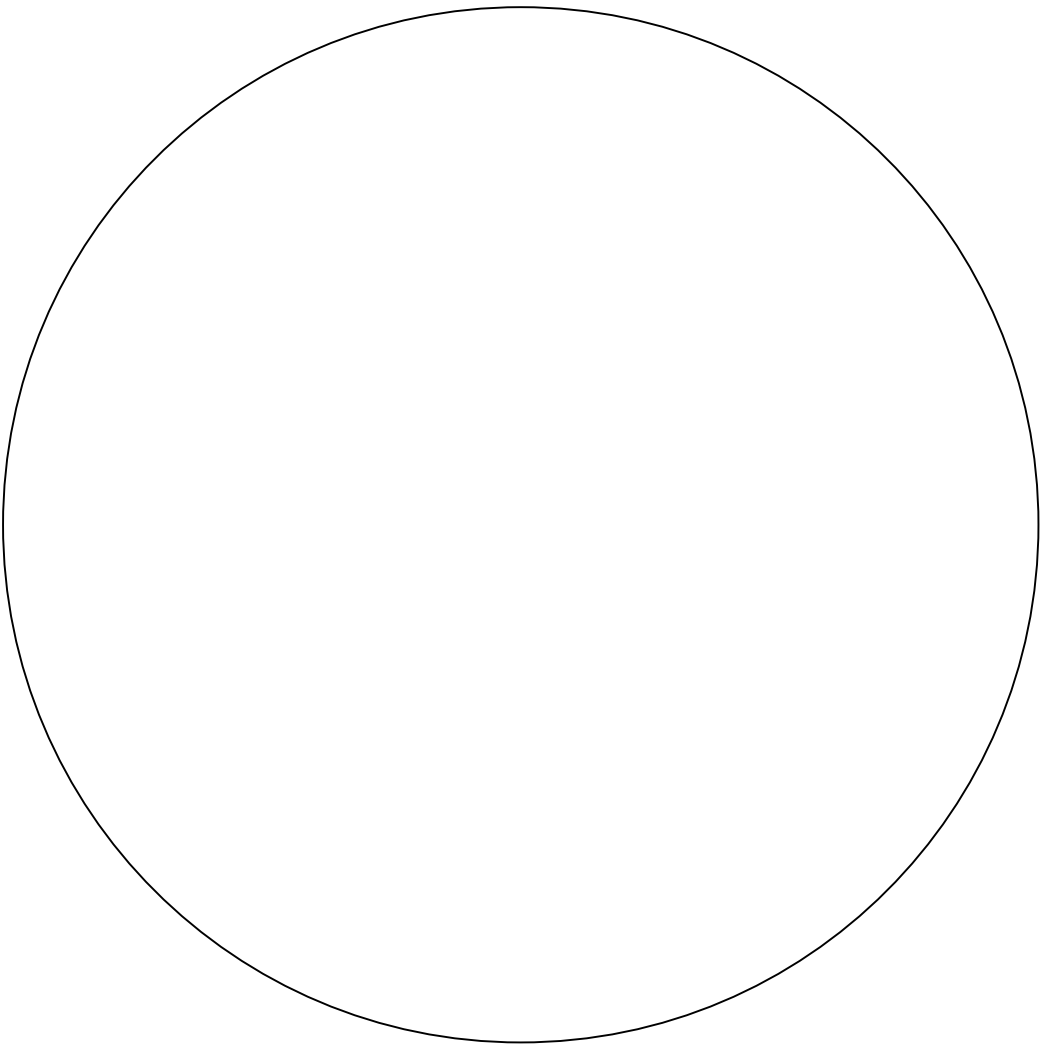
SD POWER CONTROL



SIDEKEY



FP Indicator LEDs



COMPANY: TRANSSION HOLDINGS				MODEL: A6262		Modified Date: 2018/11/14	
DRAWN	DJF/TS	DATED	20180809	TITLE: 65_PERI_SIM_SD_LED_SIDEKEY		VERSION: V1.0	SHEET: 24 OF 24
CHECKED	<CHECKED>	DATED	< >	Confidentiality	CONFIDENTIAL		